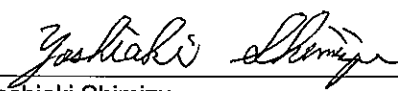




TEST DATA OF LFA75F-36

Regulated DC Power Supply
August 10, 2009

Approved by : 
Yoshiaki Shimizu Design Manager

Prepared by : 
Koji Takahashi Design Engineer

COSEL CO.,LTD.

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Model	LFA75F-36																																																					
Item	Input Current (by Load Current)	Temperature	25°C																																																			
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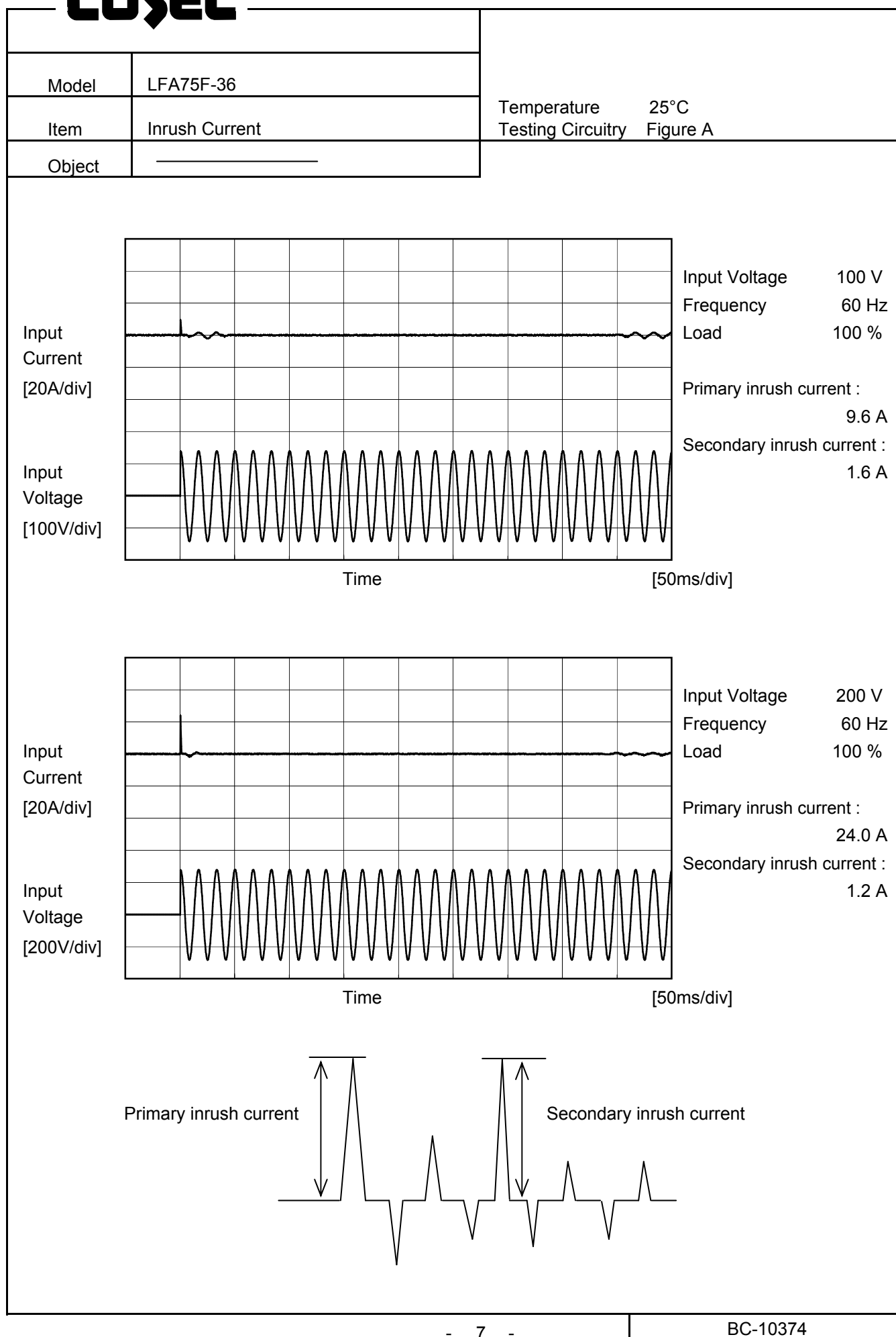
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BC-10374

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		Temperature 25°C Testing Circuitry Figure B
Model	LFA75F-36	
Item	Leakage Current	
Object	_____	

1.Results

[mA]

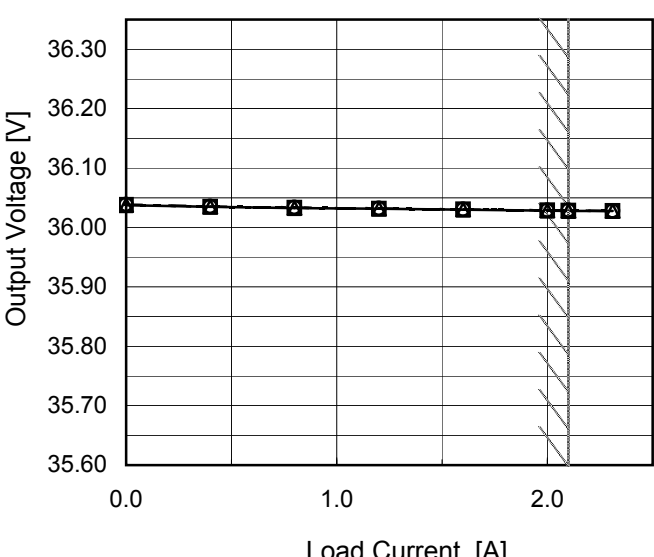
Standards		Input Volt.			Note
		100 [V]	200 [V]	240 [V]	
DEN-AN	Both phases	0.13	0.26	0.32	Operation
	One of phases	0.22	0.45	0.57	Stand by
IEC60950	Both phases	0.14	0.30	0.38	Operation
	One of phases	0.22	0.44	0.54	Stand by

The value for "One of phases" is the reference value only.

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

Model	LFA75F-36																																
Item	Line Regulation	Temperature	25°C																														
		Testing Circuitry	Figure A																														
Object	+36V2.1A																																
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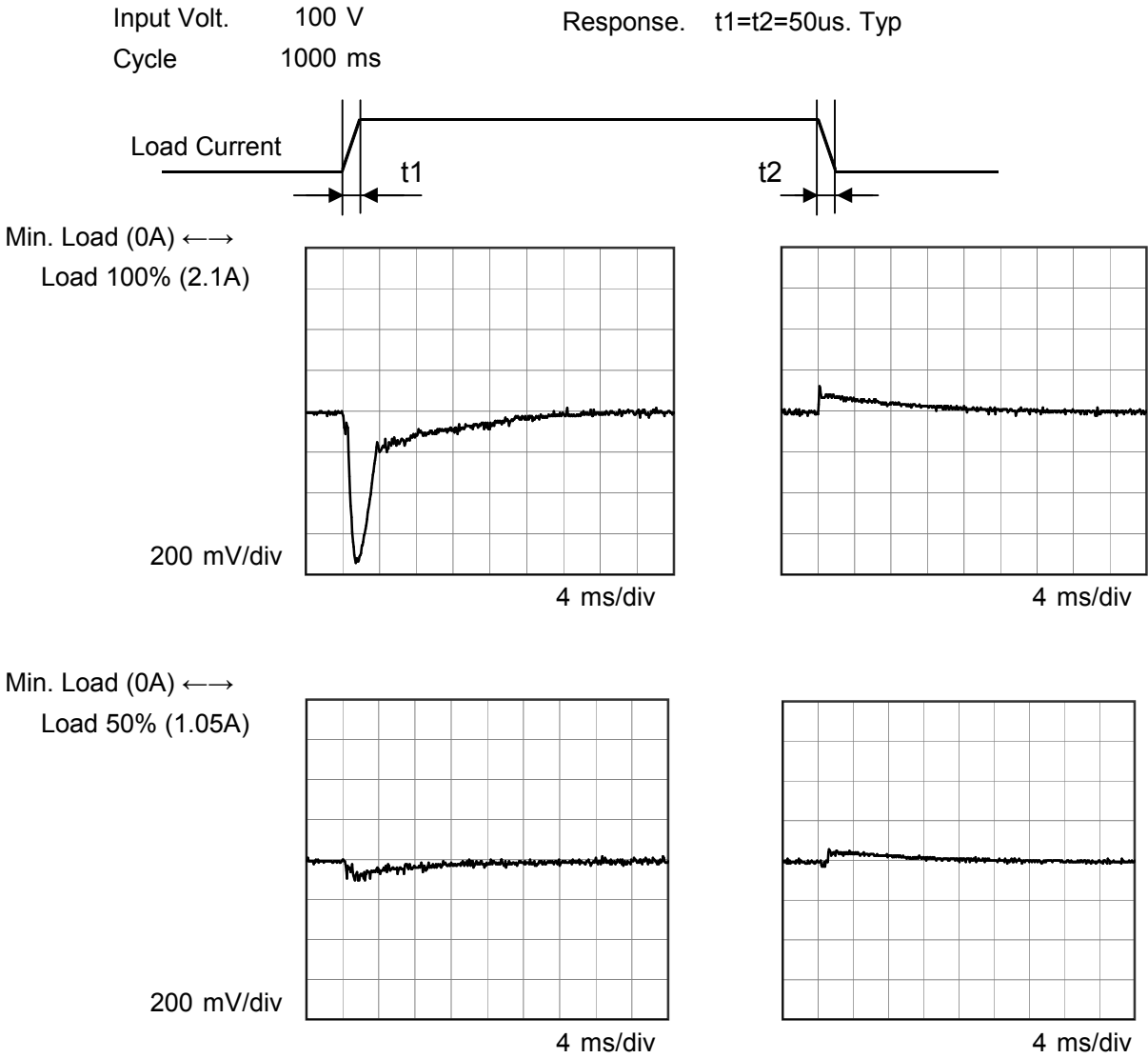
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BC-10374



Model	LFA75F-36	Temperature Testing Circuitry	25°C Figure A
Item	Dynamic Load Response		
Object	+36V2.1A		



Model	LFA75F-36	Temperature Testing Circuitry	25°C Figure C																																						
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<p>Measured by 20 MHz Oscilloscope. Ripple Voltage is shown as p-p in the figure below. Note: Slanted line shows the range of the rated load current.</p>																																									
<div><div><div><div></div><div>T1: Due to AC Input Line</div><div>T2: Due to Switching</div></div><div><p>Ripple [mVp-p]</p><p>T1</p><p>T2</p></div></div></div>																																									
Fig. Complex Ripple Wave Form																																									

Model	LFA75F-36																																								
Item	Ripple-Noise	Temperature	25°C																																						
Object	+36V2.1A	Testing Circuitry	Figure C																																						
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Load Current [A]	Ripple-Noise [mV]																																								
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Model	LFA75F-36																																																					
Item	Ambient Temperature Drift	Testing Circuitry Figure A																																																				
Object	+36V2.1A																																																					
1.Graph		2.Values																																																				
<div><div><div>—△—</div><div>---□---</div><div>-·-○-·-</div></div><div>Input Volt. 100V</div><div>Input Volt. 200V</div><div>Input Volt. 230V</div></div> <div><p>Output Voltage [V]</p><p>Ambient Temperature [°C]</p><p>Load 100%</p></div>		<table><tr><th rowspan="2">Ambient Temperature [°C]</th><th colspan="3">Output Voltage [V]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 200[V]</th><th>Input Volt. 230[V]</th></tr><tr><td>-20</td><td>36.005</td><td>36.005</td><td>36.006</td></tr><tr><td>-10</td><td>36.014</td><td>36.014</td><td>36.015</td></tr><tr><td>0</td><td>36.022</td><td>36.022</td><td>36.023</td></tr><tr><td>10</td><td>36.033</td><td>36.033</td><td>36.034</td></tr><tr><td>20</td><td>36.046</td><td>36.047</td><td>36.047</td></tr><tr><td>25</td><td>36.056</td><td>36.056</td><td>36.056</td></tr><tr><td>30</td><td>36.060</td><td>36.060</td><td>36.060</td></tr><tr><td>40</td><td>36.060</td><td>36.060</td><td>36.061</td></tr><tr><td>50</td><td>36.059</td><td>36.059</td><td>36.059</td></tr><tr><td>60</td><td>36.052</td><td>36.052</td><td>36.052</td></tr><tr><td>--</td><td>-</td><td>-</td><td>-</td></tr></table>		Ambient Temperature [°C]	Output Voltage [V]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	-20	36.005	36.005	36.006	-10	36.014	36.014	36.015	0	36.022	36.022	36.023	10	36.033	36.033	36.034	20	36.046	36.047	36.047	25	36.056	36.056	36.056	30	36.060	36.060	36.060	40	36.060	36.060	36.061	50	36.059	36.059	36.059	60	36.052	36.052	36.052	--	-	-	-
Ambient Temperature [°C]	Output Voltage [V]																																																					
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--	-	-	-																																																			
Note: Slanted line shows the range of the rated ambient temperature.																																																						

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		Testing Circuitry Figure A
Model	LFA75F-36	
Item	Output Voltage Accuracy	
Object	+36V2.1A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -10 - 50°C

Input Voltage : 85 - 264V

Load Current : 0 - 2.1A

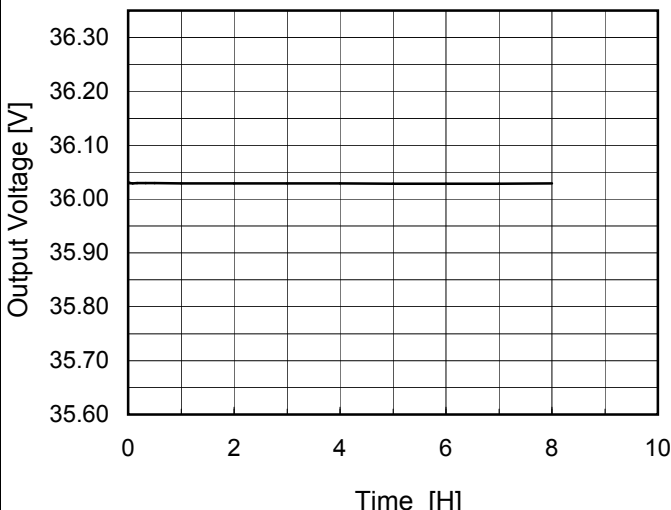
* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	40	85	0	36.069	±28	±0.1
Minimum Voltage	-10	85	2.1	36.014		



Model	LFA75F-36																								
Item	Time Lapse Drift	Temperature	25℃																						
		Testing Circuitry	Figure A																						
Object	+36V2.1A																								
1.Graph		2.Values																							
<div><p>Output Voltage [V]</p><p>Time [H]</p><p>Input Volt. 100V</p><p>Load 100%</p></div>		<table><tr><th>Time since start [H]</th><th>Output Voltage [V]</th></tr><tr><td>0.0</td><td>36.029</td></tr><tr><td>0.5</td><td>36.030</td></tr><tr><td>1.0</td><td>36.029</td></tr><tr><td>2.0</td><td>36.029</td></tr><tr><td>3.0</td><td>36.029</td></tr><tr><td>4.0</td><td>36.029</td></tr><tr><td>5.0</td><td>36.029</td></tr><tr><td>6.0</td><td>36.029</td></tr><tr><td>7.0</td><td>36.029</td></tr><tr><td>8.0</td><td>36.029</td></tr></table>		Time since start [H]	Output Voltage [V]	0.0	36.029	0.5	36.030	1.0	36.029	2.0	36.029	3.0	36.029	4.0	36.029	5.0	36.029	6.0	36.029	7.0	36.029	8.0	36.029
Time since start [H]	Output Voltage [V]																								
0.0	36.029																								
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7.0	36.029																								
8.0	36.029																								
* The characteristic of AC200V is equal.																									

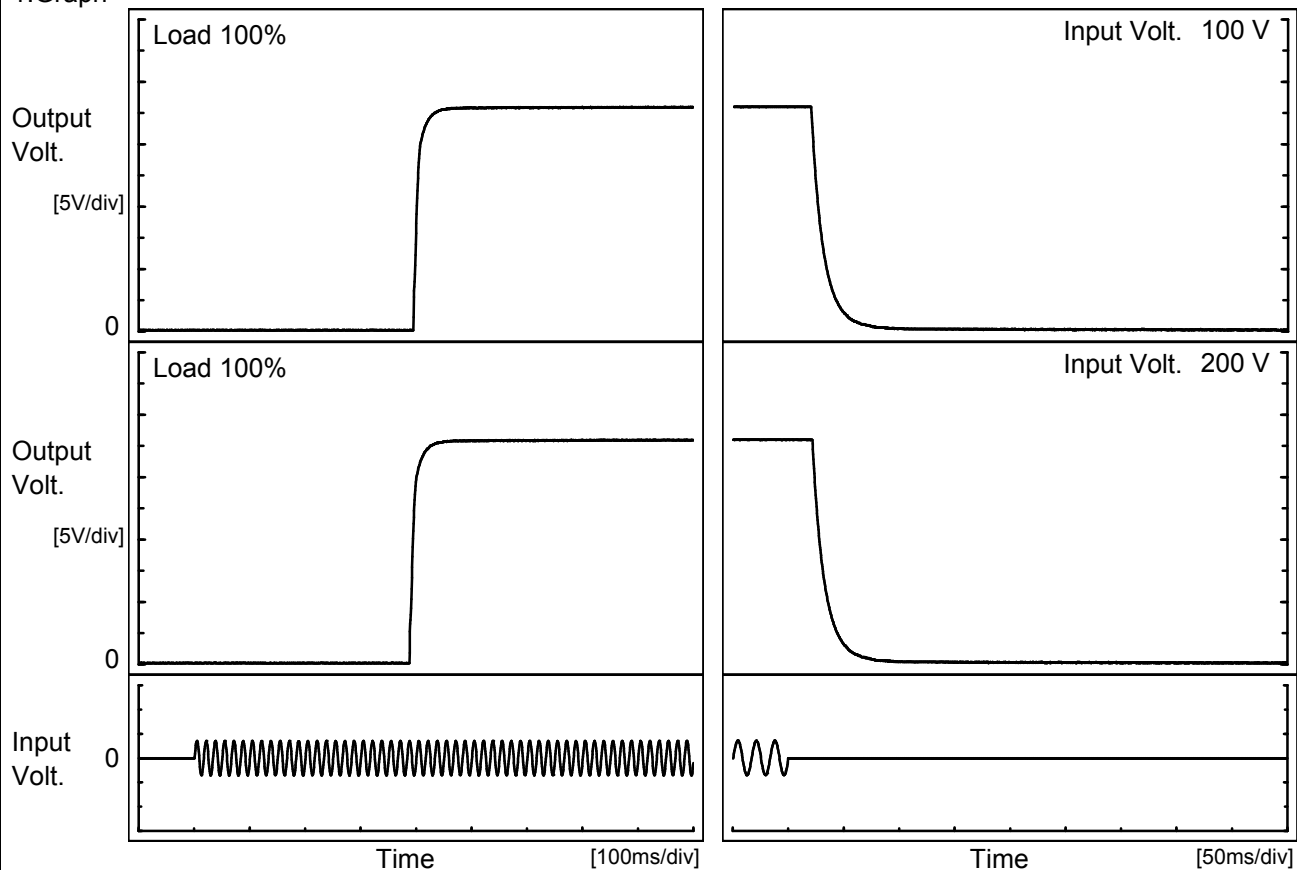
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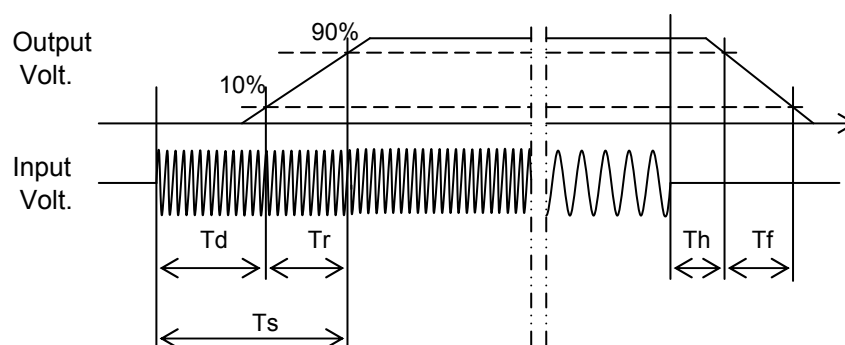
Model	LFA75F-36	Temperature	25°C
Item	Rise and Fall Time	Testing Circuitry	Figure A
Object	+36V2.1A		

1.Graph



2.Values

Input Volt.	Time	Td	Tr	Ts	Th	Tf
100 V		396.0	19.5	415.5	21.3	25.3
200 V		389.0	19.5	408.5	22.8	25.3



Model	LFA75F-36																																		
Item	Hold-Up Time	Temperature	25°C																																
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<div><div><div>---</div><div>□</div><div>---</div><div>Load 50%</div></div><div><div>—</div><div>△</div><div>—</div><div>Load 100%</div></div></div> <p>Hold-Up Time [ms]</p> <p>Input Voltage [V]</p> <p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy. Note: Slanted line shows the range of the rated input voltage.</p>		<table><tr><th rowspan="2">Input Voltage [V]</th><th colspan="2">Hold-Up Time [ms]</th></tr><tr><th>Load 50%</th><th>Load 100%</th></tr><tr><td>75</td><td>46</td><td>19</td></tr><tr><td>85</td><td>47</td><td>20</td></tr><tr><td>100</td><td>48</td><td>20</td></tr><tr><td>120</td><td>49</td><td>21</td></tr><tr><td>200</td><td>50</td><td>22</td></tr><tr><td>230</td><td>51</td><td>22</td></tr><tr><td>264</td><td>51</td><td>23</td></tr><tr><td>280</td><td>54</td><td>23</td></tr><tr><td>--</td><td>-</td><td>-</td></tr></table>		Input Voltage [V]	Hold-Up Time [ms]		Load 50%	Load 100%	75	46	19	85	47	20	100	48	20	120	49	21	200	50	22	230	51	22	264	51	23	280	54	23	--	-	-
Input Voltage [V]	Hold-Up Time [ms]																																		
	Load 50%	Load 100%																																	
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100	48	20																																	
120	49	21																																	
200	50	22																																	
230	51	22																																	
264	51	23																																	
280	54	23																																	
--	-	-																																	

Model	LFA75F-36																																																					
Item	Instantaneous Interruption Compensation	Temperature	25°C																																																			
Object	+36V2.1A	Testing Circuitry	Figure A																																																			
1.Graph		2.Values																																																				
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Load Current [A]	Time [ms]																																																					
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Note: Slanted line shows the range of the rated load current.																																																						

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Model	LFA75F-36																																											
Item	Overcurrent Protection	Temperature	25°C																																									
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Object	+36V2.1A																																											
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<div><div><div>△</div><div>Input Volt. 100V</div></div><div><div>○</div><div>Input Volt. 200V</div></div></div> <p>Note: Slanted line shows the range of the rated load current.</p> <p>Intermittent operation occurs when the output voltage is less than rated output voltage.</p>		<table><tr><th rowspan="2">Output Voltage [V]</th><th colspan="2">Load Current [A]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 200[V]</th></tr><tr><td>36.0</td><td>2.51</td><td>2.52</td></tr><tr><td>34.2</td><td>-</td><td>-</td></tr><tr><td>32.4</td><td>-</td><td>-</td></tr><tr><td>28.8</td><td>-</td><td>-</td></tr><tr><td>25.2</td><td>-</td><td>-</td></tr><tr><td>21.6</td><td>-</td><td>-</td></tr><tr><td>18.0</td><td>-</td><td>-</td></tr><tr><td>14.4</td><td>-</td><td>-</td></tr><tr><td>10.8</td><td>-</td><td>-</td></tr><tr><td>7.2</td><td>-</td><td>-</td></tr><tr><td>3.6</td><td>-</td><td>-</td></tr><tr><td>0.0</td><td>-</td><td>-</td></tr></table>		Output Voltage [V]	Load Current [A]		Input Volt. 100[V]	Input Volt. 200[V]	36.0	2.51	2.52	34.2	-	-	32.4	-	-	28.8	-	-	25.2	-	-	21.6	-	-	18.0	-	-	14.4	-	-	10.8	-	-	7.2	-	-	3.6	-	-	0.0	-	-
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0.0	-	-																																										

Model		LFA75F-36	Testing Circuitry Figure A
Item		Overvoltage Protection	
Object		+36V2.1A	
1.Graph			2.Values
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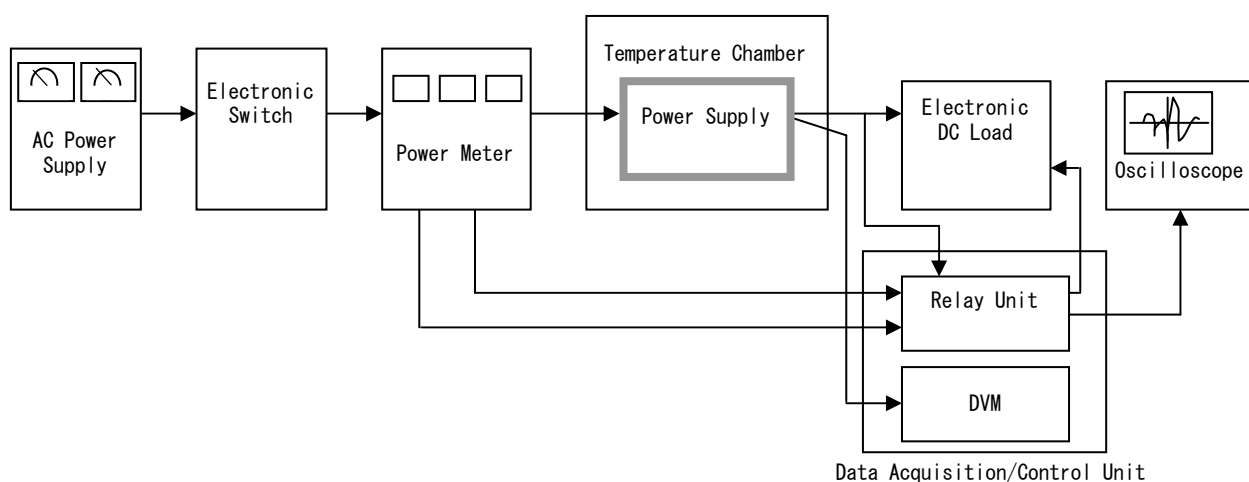


Figure A

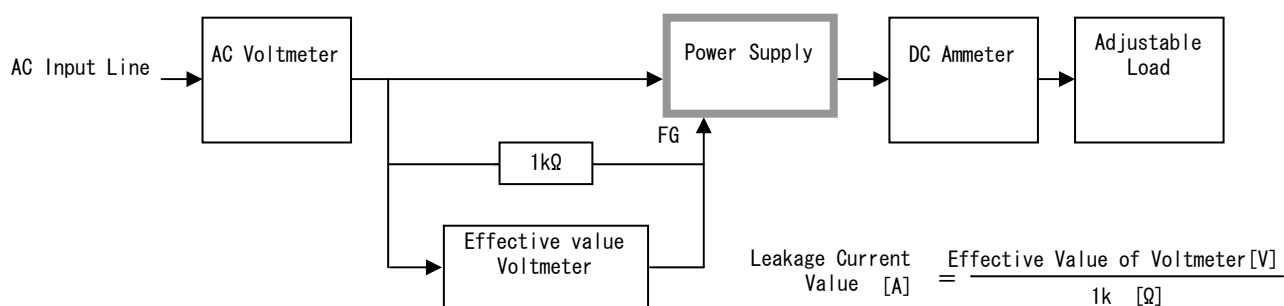


Figure B (DEN-AN)

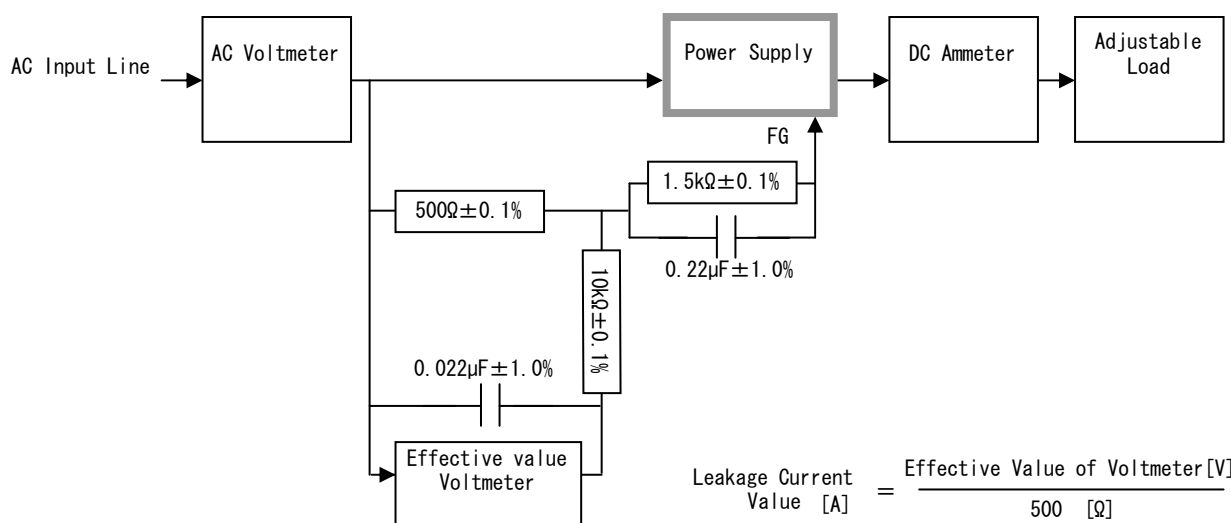


Figure B (IEC60950-1)

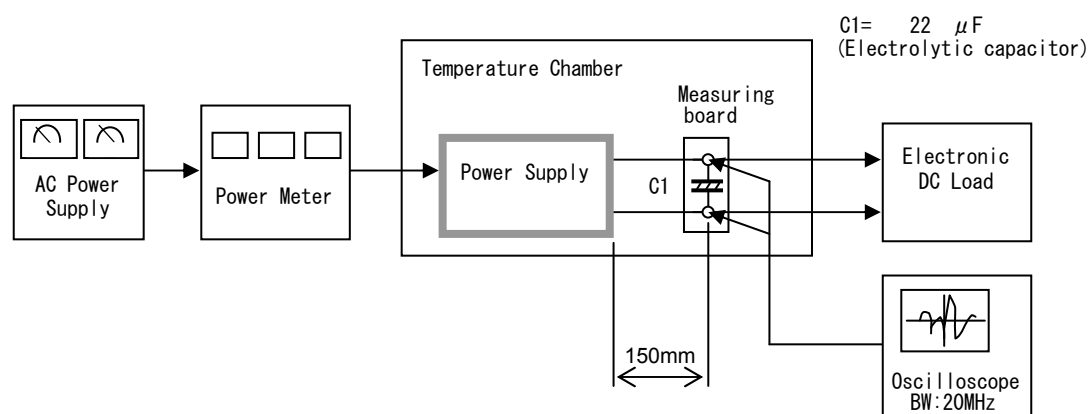


Figure C