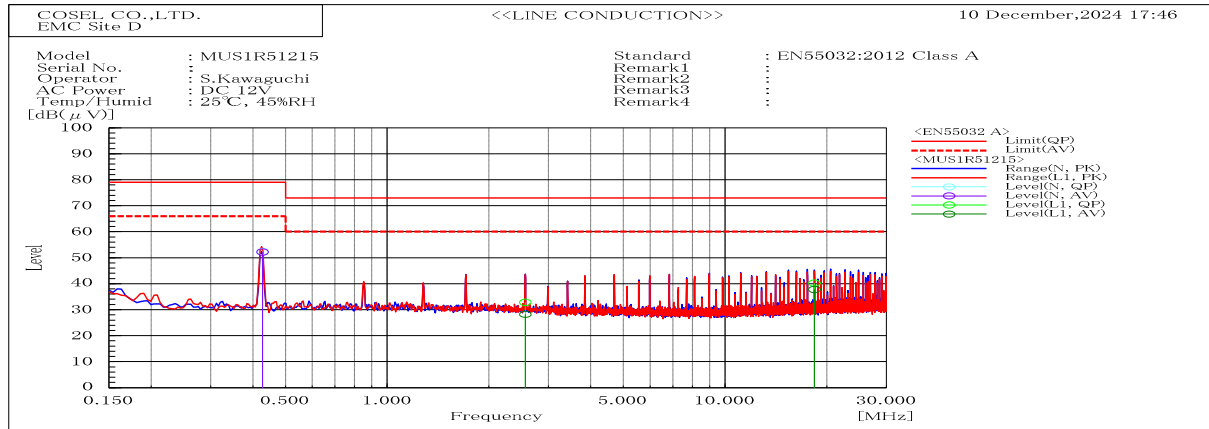
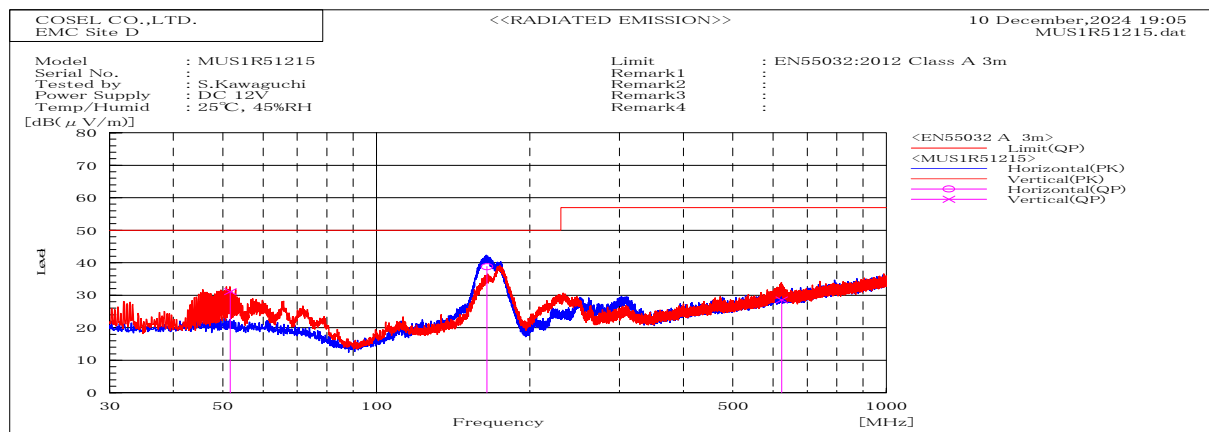


DATA SHEET

Model		MUS1R51215	Date	05-Mar-25
Test		EMI Line conduction & Radiated emission	Temp.	25 degreeC
			Humid.	45 %RH
			Tested by	S.Kawaguchi



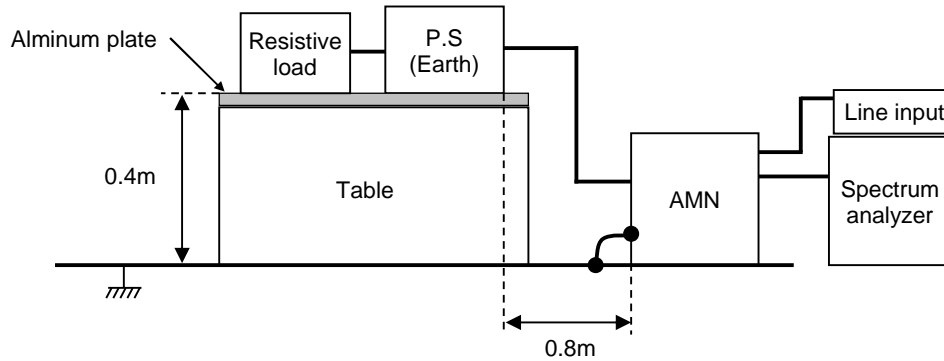
Frequency MHz	Line	Level dB(μV)		Limit dB(μV)		Margin dB		Pass/Fail	Remark
		QP	AV	QP	AV	QP	AV		
0.427	N	52.4	52.2	79	66	26.6	13.8	Pass	
2.561	L1	32.8	28.2	73	60	40.2	31.8	Pass	
18.383	L1	40.2	37.8	73	60	32.8	22.2	Pass	



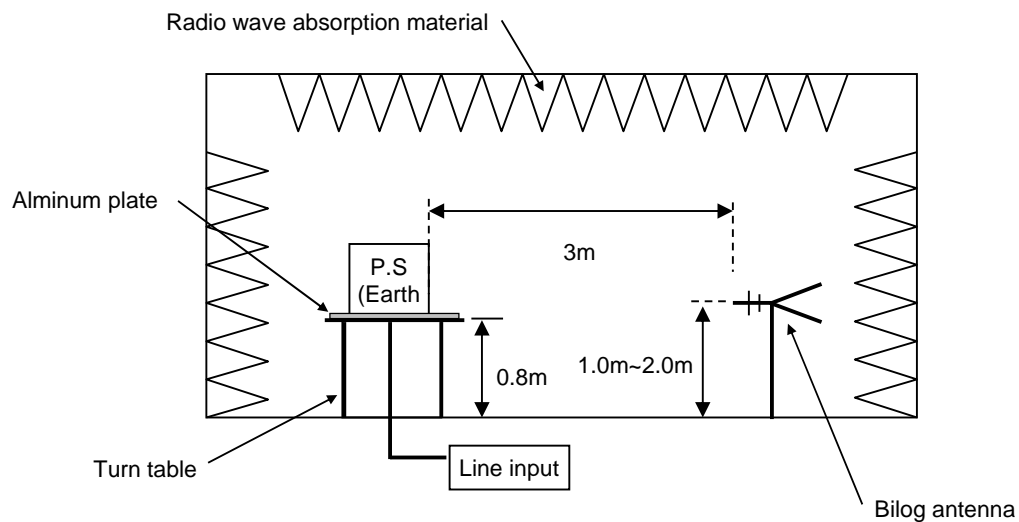
Frequency MHz	Polarization	Stability	Level dB(μV/m)		Limit dB(μV/m)	Margin dB	Pass/Fail	Height cm	Angle deg	Remark
			QP	QP		QP				
164.775	H	Stable	38.8	50	11.2	Pass	183.9	225		
51.72	V	Stable	31.1	50	18.9	Pass	100.2	213		
623.63	V	Stable	28.6	57	28.4	Pass	100.2	103		

DATA SHEET		Date	05-Mar-25
Model	Circuit used for measurement	Temp.	25 degreeC
Test	EMI Line conduction & Radiated emission	Humid.	45 %RH
		Tested by	S.Kawaguchi

1. Line conduction



2. Radiated emission



Conditions

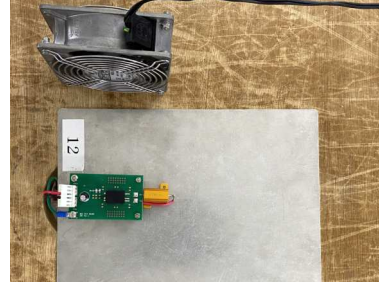
Test : EMI
Model Name: MUS1R5□□

○Photographs of Test Set-Up

LINE CONDUCTION



RADIATED EMISSION



○Testing circuitry

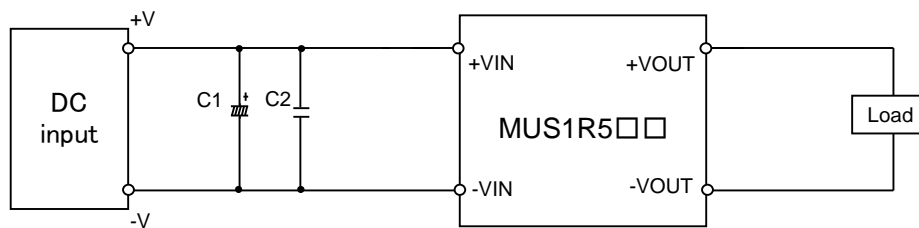


Fig.1 MUS1R505□, MUS1R512□, MUS1R524□ Testing circuitry

C1 :	MUS1R505□	16V 220 μ F	Electric capacitor (UPWseries NICHICON)
	MUS1R512□	50V 100 μ F	Electric capacitor (UPWseries NICHICON)
	MUS1R524□	-	
C2 :	MUS1R505□	16V 22 μ F	Ceramic capacitor (GRM31CC71C226M MURATA MANUFACTURING)
	MUS1R512□	25V 22 μ F	Ceramic capacitor (C3216JB1E226MT TDK)
	MUS1R524□	50V 10 μ F	Ceramic capacitor (C3216X7R1H106KT TDK)

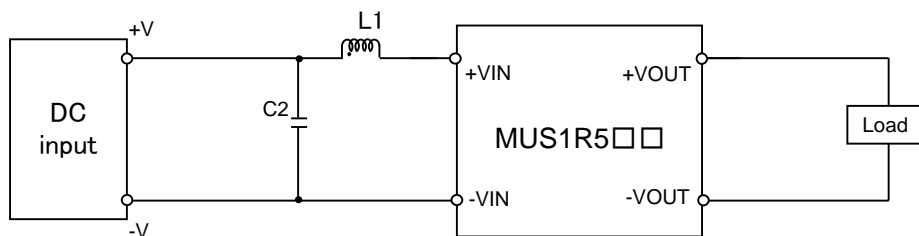


Fig.2 MUS1R548□ Testing circuitry

C2 :	MUS1R548□	100V 2.2 μ F	Ceramic capacitor (C3216X7S2A225KT TDK)
L1 :	MUS1R548□	520mA 15 μ H	Inductor (LQH32PN150MN0L MURATA MANUFACTURING)