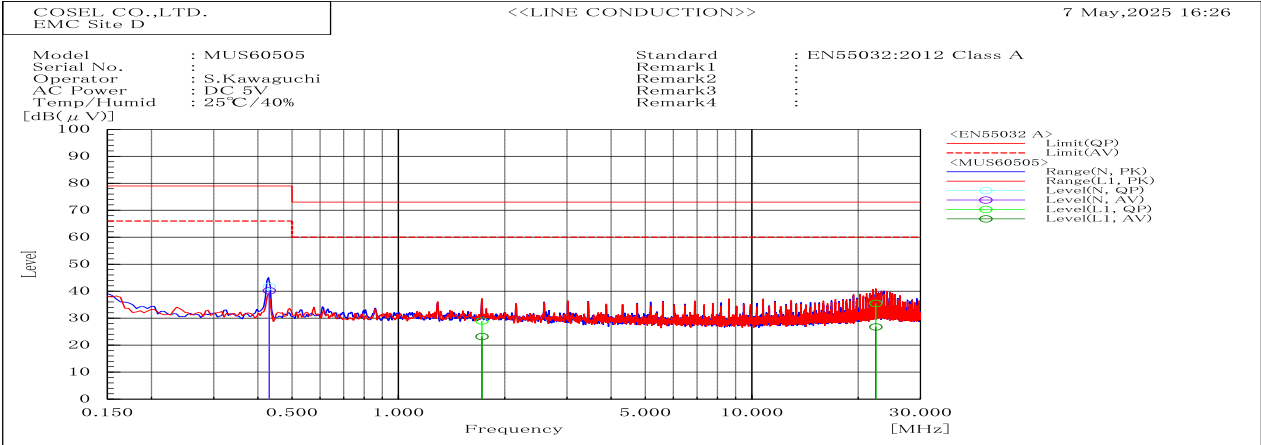
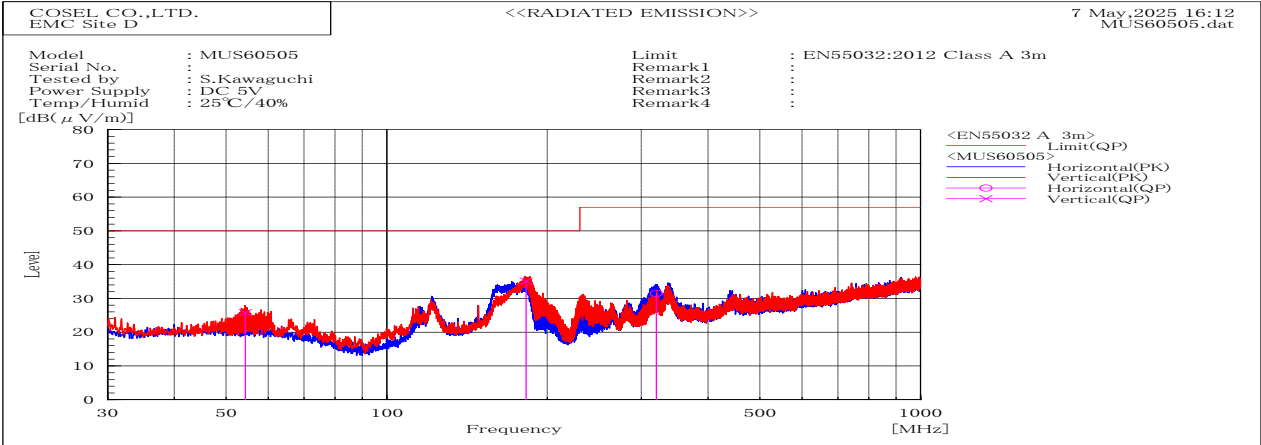


DATA SHEET		Date	07-May-25
Model	MUS60505	Temp.	25 degreeC
Test	EMI Line conduction & Radiated emission	Humid.	40 %RH
		Tested by	S.Kawaguchi



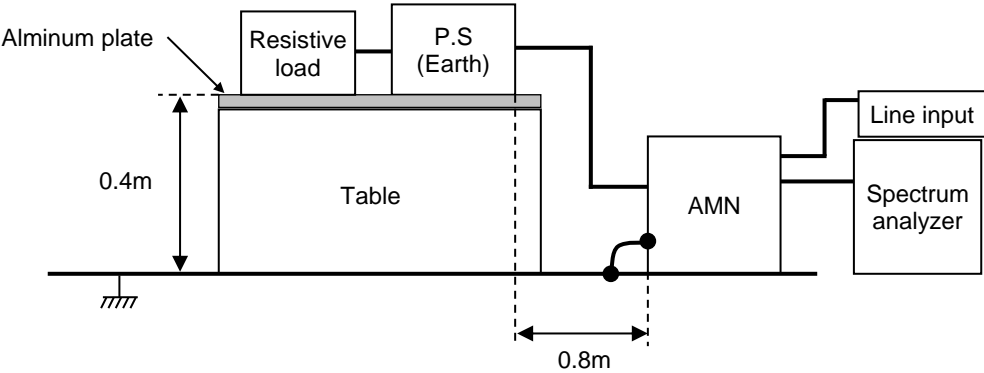
Frequency	Line	Level		Limit		Margin		Pass/Fail	Remark
MHz		dB(μV)		dB(μV)		dB			
		QP	AV	QP	AV	QP	AV		
1.723	L1	28.9	23.2	73	60	44.1	36.8	Pass	
22.433	L1	35.5	26.7	73	60	37.5	33.3	Pass	
0.431	N	41.8	40.2	79	66	37.2	25.8	Pass	



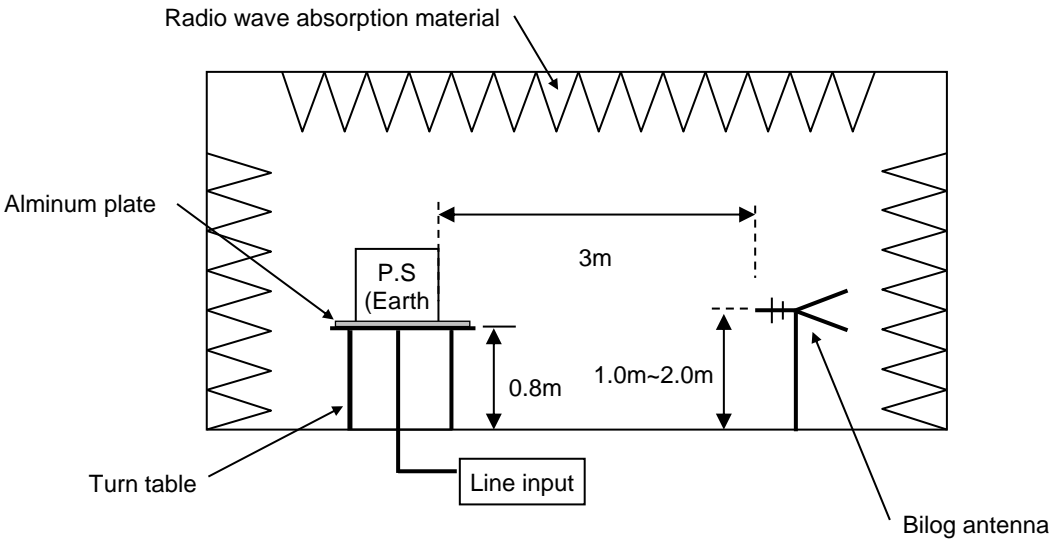
Frequency MHz	Polarization	Stability	Level	Limit	Margin	Pass/Fail	Height cm	Angle deg	Remark
			dB(μV/m) QP	dB(μV/m) QP	dB QP				
182.358	V	Stable	35.1	50	14.9	Pass	100.2	0	
54.328	V	Stable	25.8	50	24.2	Pass	100.2	0	
319.875	H	Stable	31.5	57	25.5	Pass	100.2	70.9	

DATA SHEET		Date	07-May-25
Model	Circuit used for measurement	Temp.	25 degreeC
Test	EMI Line conduction & Radiated emission	Humid.	40 %RH
		Tested by	S.Kawaguchi

1. Line conduction



2. Radiated emission



Conditions

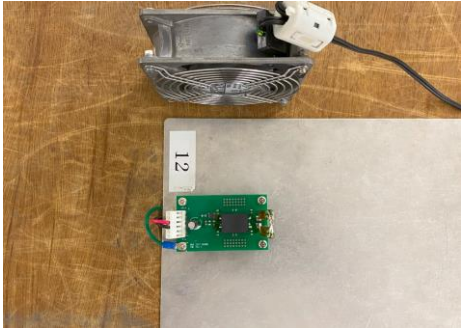
Test : EMI  
Model Name: MUS6□□

○Photographs of Test Set-Up

LINE CONDUCTION



RADIATED EMISSION



○Testing circuitry

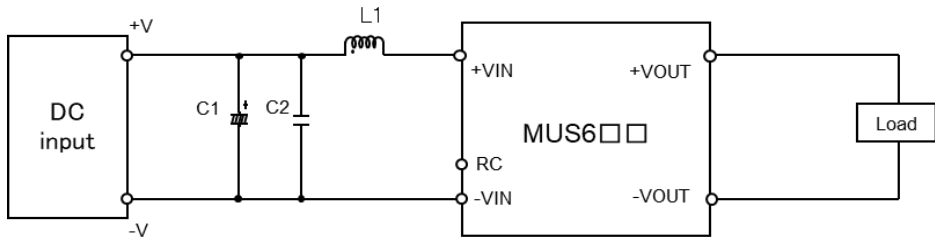


Fig.1 MUS6□□ Testing circuitry

C1 :	MUS605□	16V 220 $\mu$ F	Electric capacitor (UPWseries NICHICON)
	MUS612□	50V 100 $\mu$ F	Electric capacitor (UPWseries NICHICON)
	MUS624□	—	
	MUS648□	—	
C2 :	MUS605□	16V 22 $\mu$ F	Ceramic capacitor (GRM31CC71C226M MURATA MANUFACTURING)
	MUS612□	25V 10 $\mu$ F	Ceramic capacitor (CM316X7R106K25AT KYOCERA)
	MUS624□	50V 4.7 $\mu$ F	Ceramic capacitor (GRM31CR71H475K MURATA MANUFACTURING)
	MUS648□	100V 2.2 $\mu$ F	Ceramic capacitor (C3216X7S2A225KT TDK)
L1 :	MUS605□	6500mA 1.5 $\mu$ H	Inductor(LQH5BPN1R5N38 MURATA MANUFACTURING)
	MUS612□	5000mA 2.2 $\mu$ H	Inductor(LQH5BPN2R2N38 MURATA MANUFACTURING)
	MUS624□	2600mA 10 $\mu$ H	Inductor(LQH5BPN100M38 MURATA MANUFACTURING)
	MUS648□	1600mA 22 $\mu$ H	Inductor(LQH5BPN220M38 MURATA MANUFACTURING)