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US DESIGN DEPT.

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No.	Test item	Conditions	Conditions of acceptability	Result
1	Overload test	(1)Overload. 135% of current rating (2)Ambient temp. $25 \pm 5^{\circ}\text{C}$ (3)Test period 48 hours	(1)No physical damage	OK
2	Withstand voltage test (High-pot test)	(1) Input Not applied. (2) Ambient temp. $25 \pm 10^{\circ}\text{C}$ (3) The applied voltage is 1.4 times of specifications.	(1)Insulation breakdown, flashover or electric arc is not occurred	OK
3	Isolation resistance test	(1) Input Not applied. (2) Ambient temp. $25 \pm 10^{\circ}\text{C}$	(1)When a regulation voltage is applied, isolation resistance is 1.4 times of specifications.	OK
4	Vibration/impact test	Vibration (1) $f=10 \sim 150\text{Hz}$: 29.4m/s^2 (2)3 minutes period (3)60 minutes along X, Y and Z axis Impact (1) 294.2m/s^2 11ms (2)Once each X, Y and Z axis	(1)No degradation of electric characteristics after test. (2)No crack at solder joint. (3)No marked damage of appearance.	OK