

# TEST DATA OF TECS20F-24

Regulated DC Power Supply  
February 20, 2025

Approved by : Tetsuro Hirata  
Design Manager

Prepared by : Junichi Otsubo  
Design Engineer

**COSEL CO.,LTD.**

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Model		TECS20F-24		Temperature Testing Circuitry	25°C Figure A																																																					
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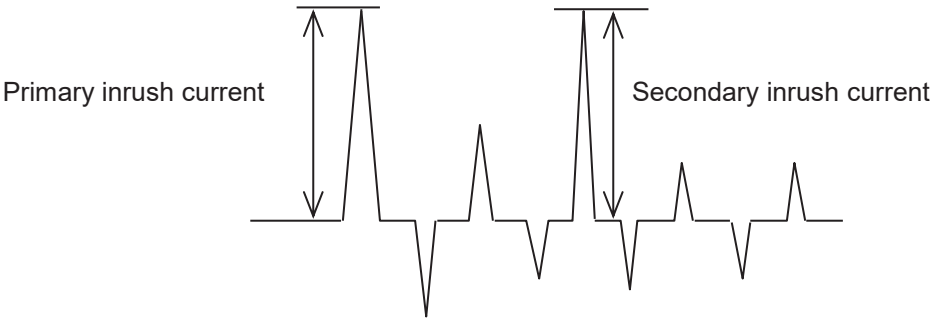
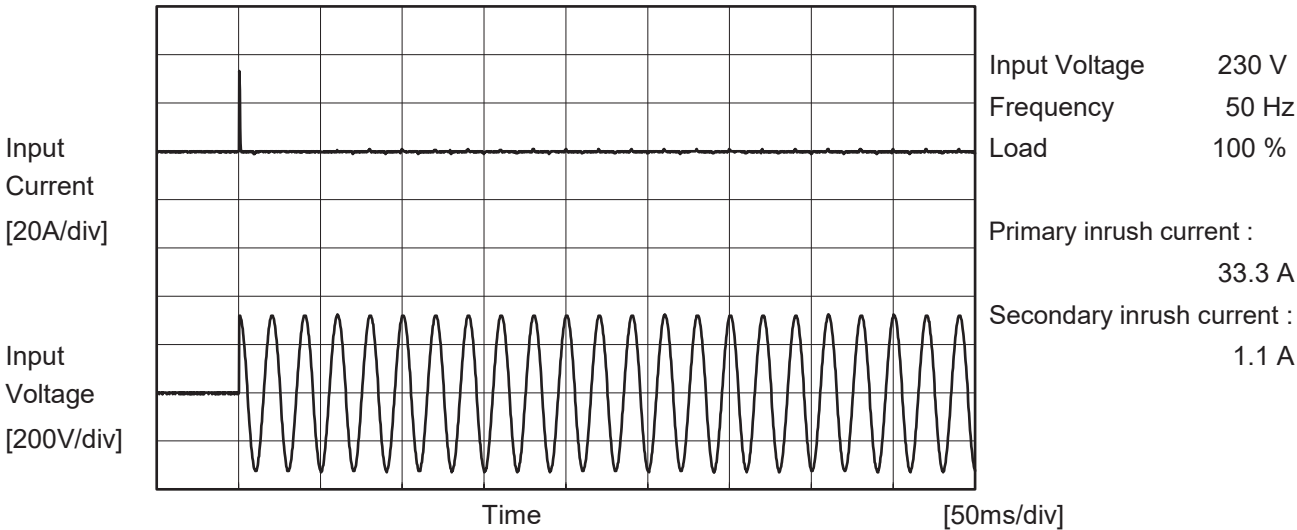
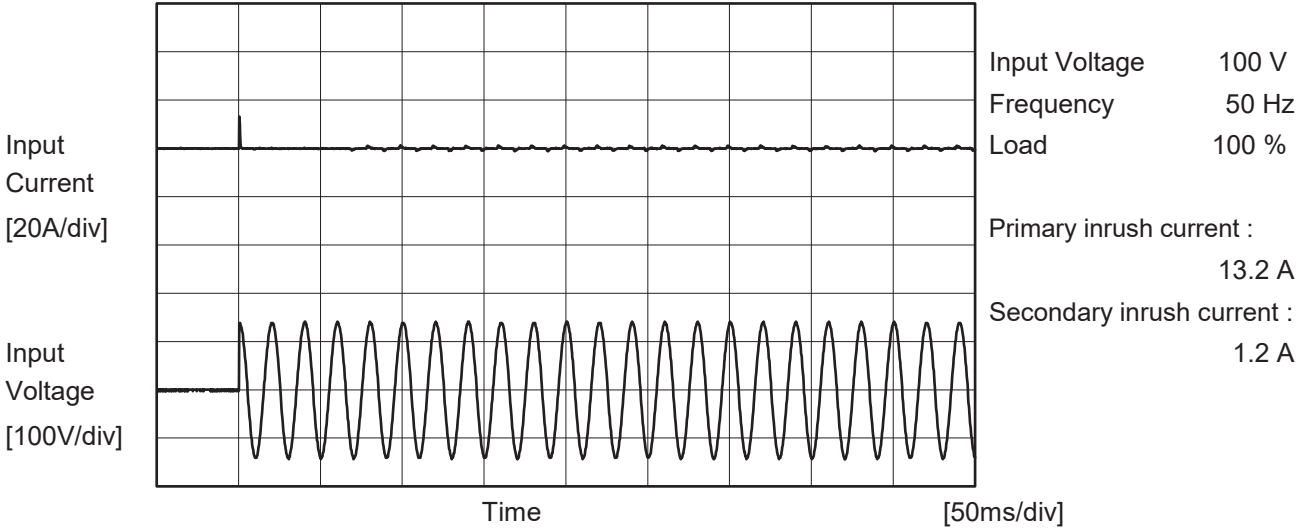
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Model		TECS20F-24	
Item		Inrush Current	Temperature 25°C Testing Circuitry Figure A
Object			





Model		TECS20F-24	Temperature 25°C Testing Circuitry Figure C
Item		Leakage Current	
Object		_____	

## 1.Results

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	23	47	54	Operation
		One of phases	28	70	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	80	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

## 2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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Item	Line Regulation																																		
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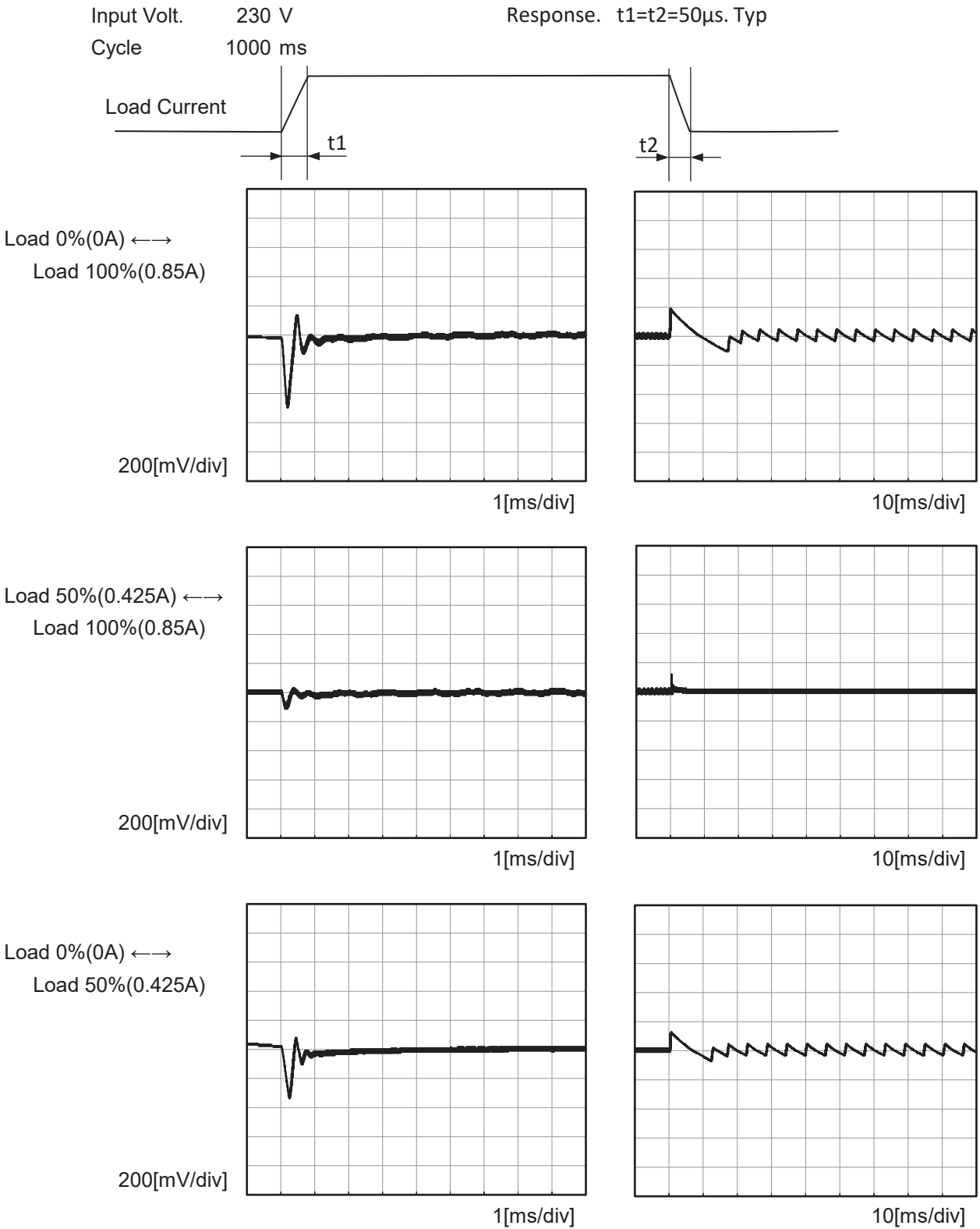
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0.680	24.127	24.129	24.129																																																									
0.765	24.127	24.128	24.128																																																									
0.850	24.127	24.127	24.127																																																									
0.935	24.121	24.124	24.124																																																									
Item		Ripple-Noise		Temperature 25°C																																																								
Object		+24V0.85A		Testing Circuitry Figure B																																																								
1.Graph		<div><div>Input Voltage230V</div><div>Load100%</div></div> <div></div> <p>20[mV/div]</p> <p>4[μs/div]</p>																																																										

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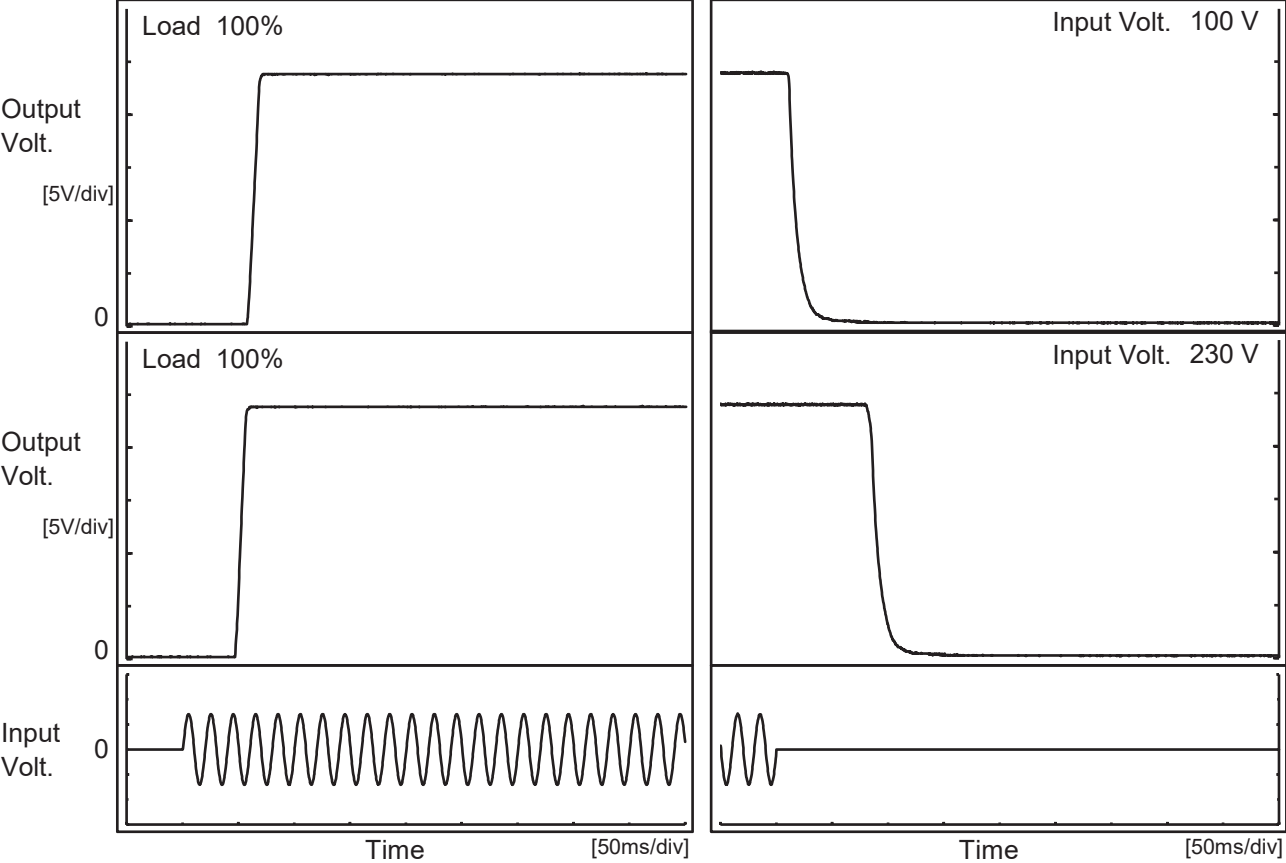
Model		TECS20F-24	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+24V0.85A	





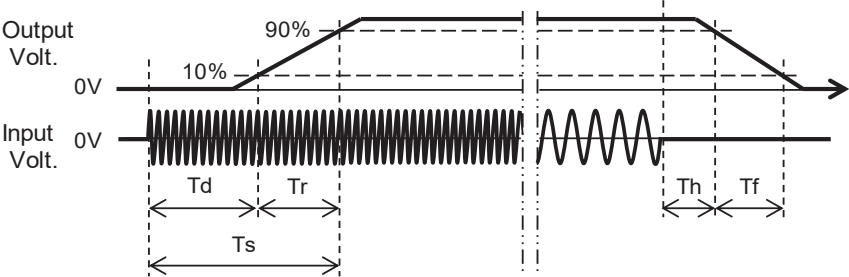
Model		TECS20F-24	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+24V0.85A	

1.Graph



2.Values

		[ms]				
Input Volt.	Time	Td	Tr	Ts	Th	Tf
100V		59.3	9.0	68.3	12.0	16.3
230V		48.3	8.0	56.3	84.5	17.0



**COSEL**

Model		TECS20F-24		Temperature 25°C																																	
Item		Hold-Up Time		Testing Circuitry Figure A																																	
Object		+24V0.85A																																			
1.Graph				2.Values																																	
<div><div><div>-----□----- Load 50%</div><div>-----△----- Load 100%</div></div><div><div>1000</div><div>100</div><div>10</div><div>1</div><div>50</div><div>100</div><div>150</div><div>200</div><div>250</div><div>300</div></div><div><div>Hold-Up Time [ms]</div><div>Input Voltage [V]</div></div></div> <div><p>This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.</p><p>Note: Slanted line shows the range of the rated input voltage.</p></div>				<table><tr><th rowspan="2">Input Voltage [V]</th><th colspan="2">Hold-Up Time [ms]</th></tr><tr><th>Load 50%</th><th>Load 100%</th></tr><tr><td>85</td><td>18</td><td>6</td></tr><tr><td>100</td><td>27</td><td>10</td></tr><tr><td>115</td><td>38</td><td>16</td></tr><tr><td>200</td><td>128</td><td>59</td></tr><tr><td>230</td><td>172</td><td>81</td></tr><tr><td>264</td><td>230</td><td>110</td></tr><tr><td>280</td><td>260</td><td>125</td></tr><tr><td>--</td><td>-</td><td>-</td></tr><tr><td>--</td><td>-</td><td>-</td></tr></table>		Input Voltage [V]	Hold-Up Time [ms]		Load 50%	Load 100%	85	18	6	100	27	10	115	38	16	200	128	59	230	172	81	264	230	110	280	260	125	--	-	-	--	-	-
Input Voltage [V]	Hold-Up Time [ms]																																				
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100	27	10																																			
115	38	16																																			
200	128	59																																			
230	172	81																																			
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Model		TECS20F-24	Temperature 25°C Testing Circuitry Figure A																																																							
Item		Instantaneous Interruption Compensation																																																								
Object		+24V0.85A																																																								
1.Graph		<div><div>—△—</div>Input Volt. 100V</div> <div><div>---□---</div>Input Volt. 200V</div> <div><div>-·-○-·-</div>Input Volt. 230V</div> <p>Instantaneous Compensation Time [ms]</p> <p>Load Current [A]</p>	2.Values																																																							
		<table><tr><th rowspan="2">Load Current [A]</th><th colspan="3">Time [ms]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 200[V]</th><th>Input Volt. 230[V]</th></tr><tr><td>0.000</td><td>-</td><td>-</td><td>-</td></tr><tr><td>0.085</td><td>148</td><td>628</td><td>835</td></tr><tr><td>0.170</td><td>74</td><td>320</td><td>430</td></tr><tr><td>0.255</td><td>48</td><td>215</td><td>287</td></tr><tr><td>0.340</td><td>35</td><td>161</td><td>216</td></tr><tr><td>0.425</td><td>27</td><td>128</td><td>172</td></tr><tr><td>0.510</td><td>22</td><td>106</td><td>143</td></tr><tr><td>0.595</td><td>18</td><td>90</td><td>122</td></tr><tr><td>0.680</td><td>15</td><td>78</td><td>105</td></tr><tr><td>0.765</td><td>13</td><td>68</td><td>93</td></tr><tr><td>0.850</td><td>11</td><td>60</td><td>82</td></tr><tr><td>0.935</td><td>8</td><td>53</td><td>72</td></tr></table>		Load Current [A]	Time [ms]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.000	-	-	-	0.085	148	628	835	0.170	74	320	430	0.255	48	215	287	0.340	35	161	216	0.425	27	128	172	0.510	22	106	143	0.595	18	90	122	0.680	15	78	105	0.765	13	68	93	0.850	11	60	82	0.935	8	53	72
Load Current [A]	Time [ms]																																																									
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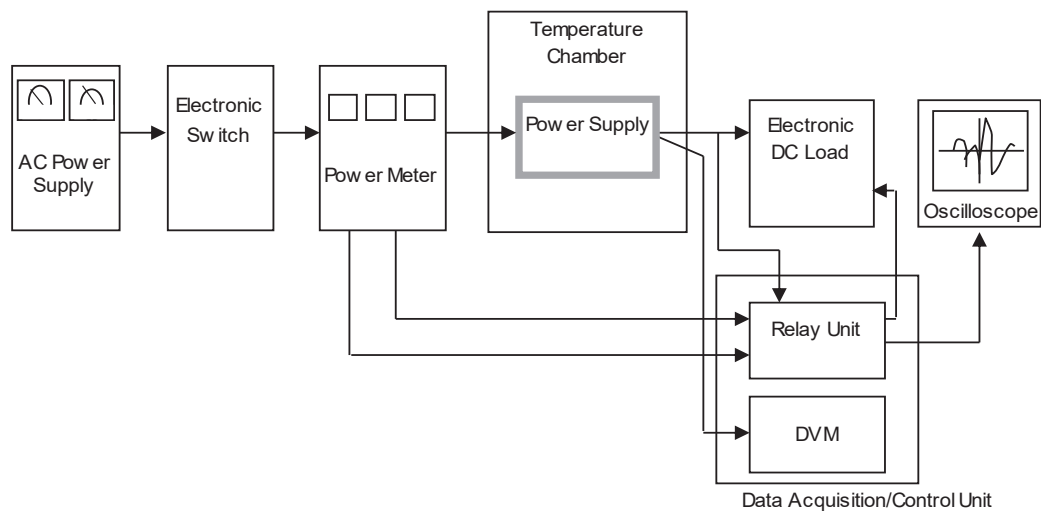


Figure A

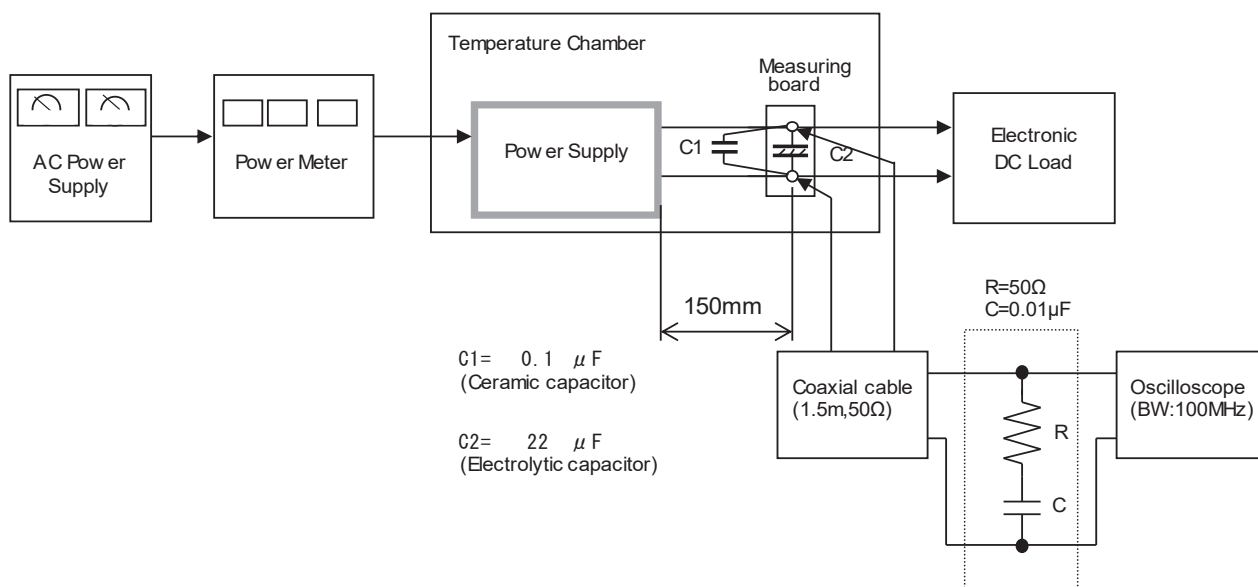


Figure B



