

TEST DATA OF TEPS10F15

Regulated DC Power Supply
February 28, 2025

Approved by : Tetsuro Hirata
Design Manager

Prepared by : Junichi Otsubo
Design Engineer

COSEL CO.,LTD.

CONTENTS

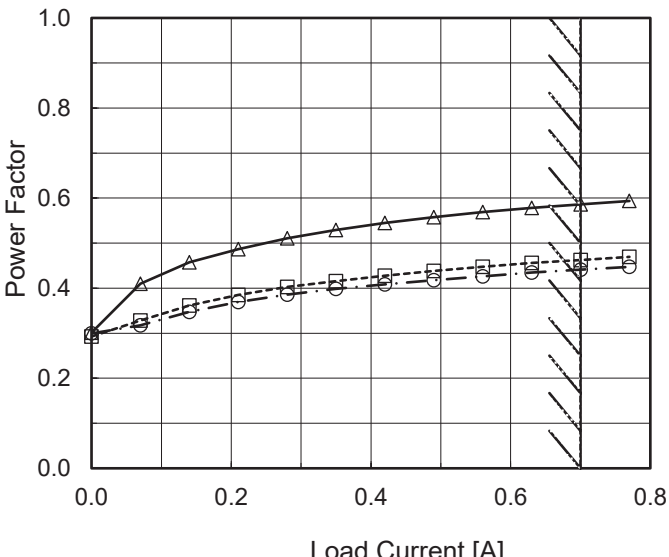
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Model		TEPS10F15		Temperature Testing Circuitry	25°C Figure A																																																					
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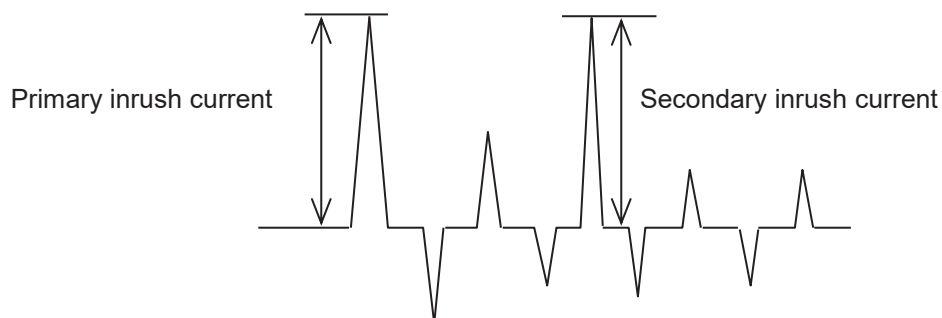
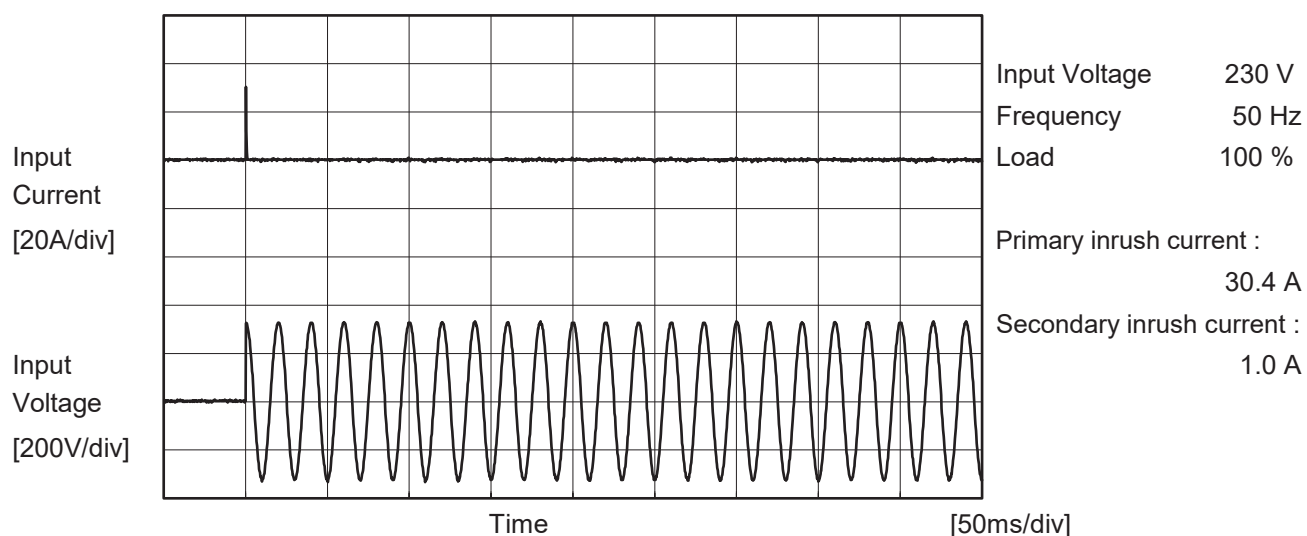
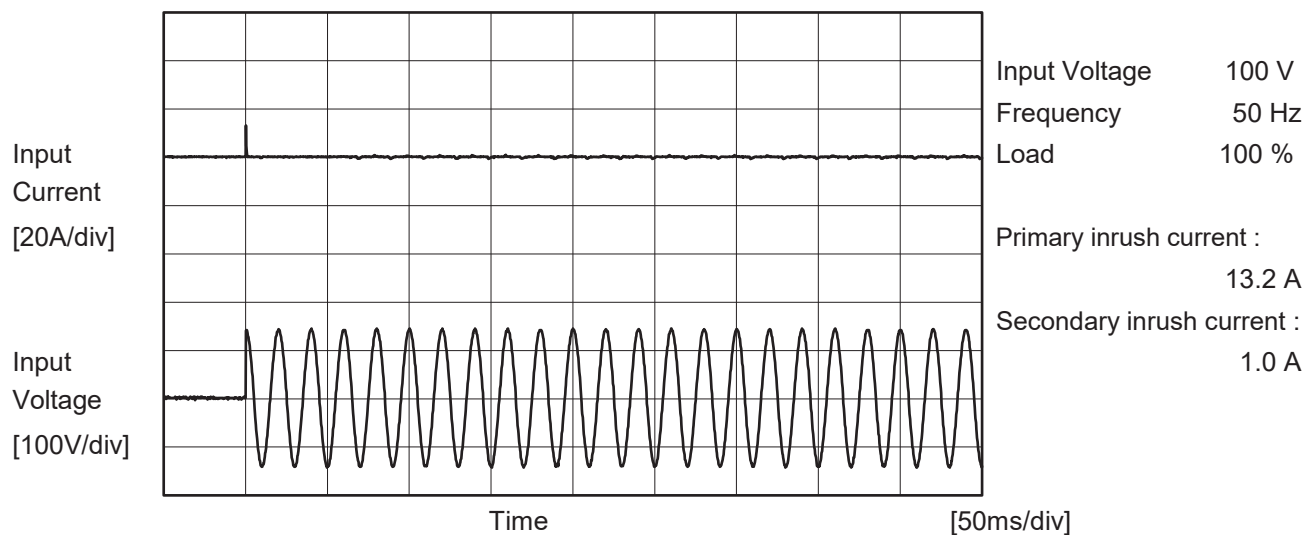
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Model	TEPS10F15	Temperature	25°C
Item	Inrush Current	Testing Circuitry	Figure A
Object	_____		





Model		TEPS10F15	Temperature 25°C Testing Circuitry Figure C
Item		Leakage Current	
Object		_____	

1.Results

[μA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	24	47	53	Operation
		One of phases	28	69	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	81	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

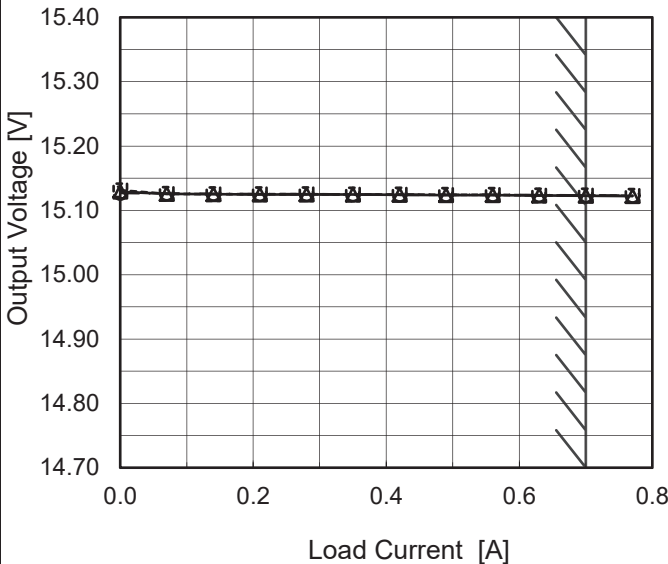
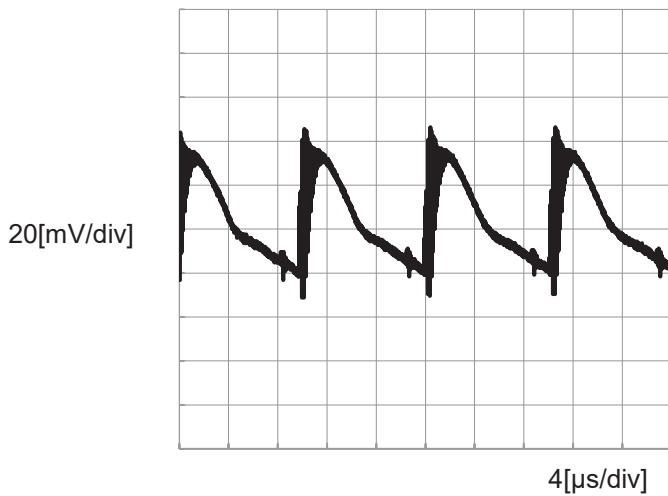
2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.



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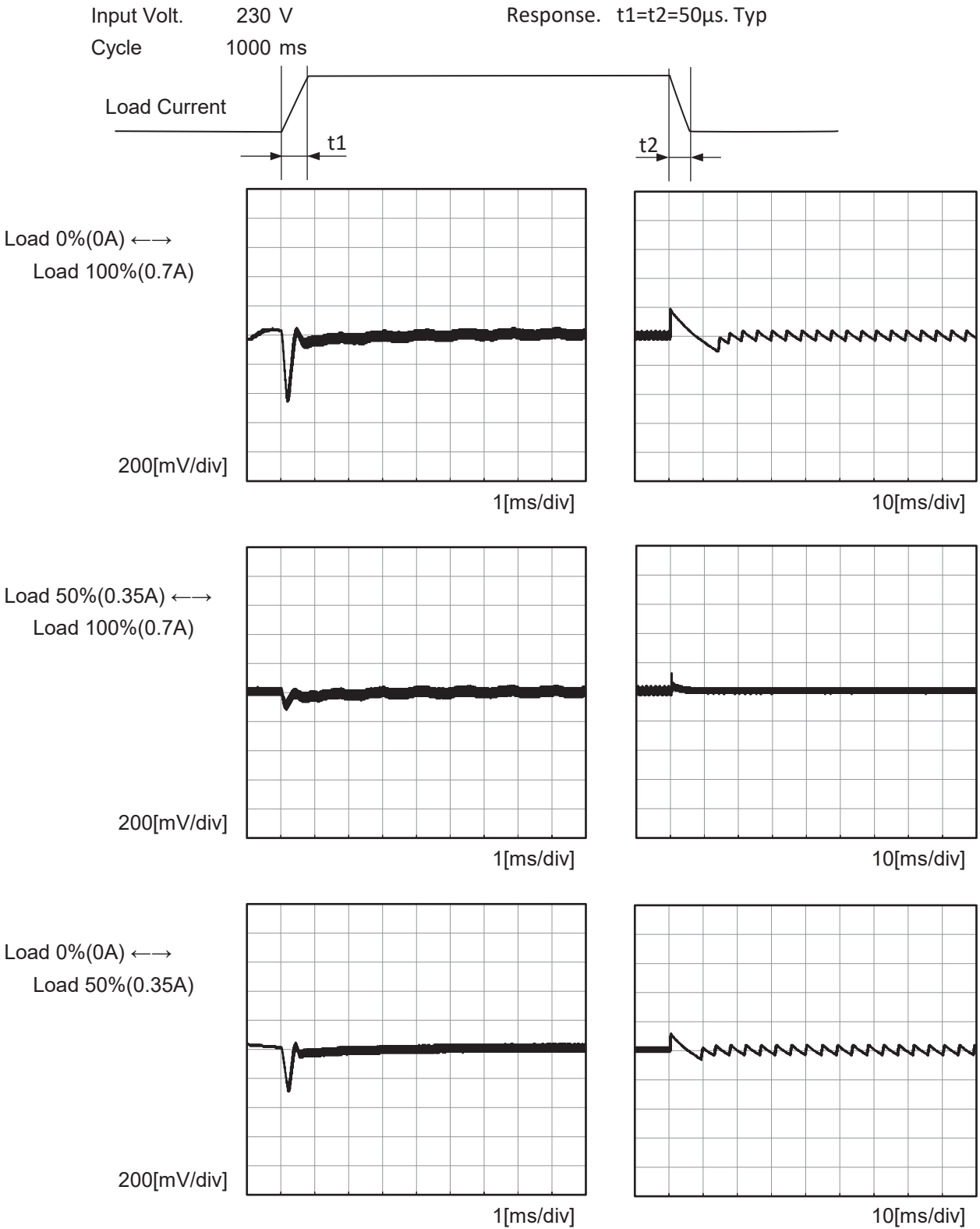
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0.49	15.124	15.124	15.124																																																							
0.56	15.124	15.124	15.124																																																							
0.63	15.123	15.123	15.124																																																							
0.70	15.123	15.123	15.123																																																							
0.77	15.122	15.123	15.123																																																							
Item	Ripple-Noise	Temperature	25°C																																																							
Object	+15V0.7A	Testing Circuitry	Figure B																																																							
1.Graph																																																										
<div><div><div>Input Voltage</div><div>230V</div></div><div><div>Load</div><div>100%</div></div></div> 																																																										

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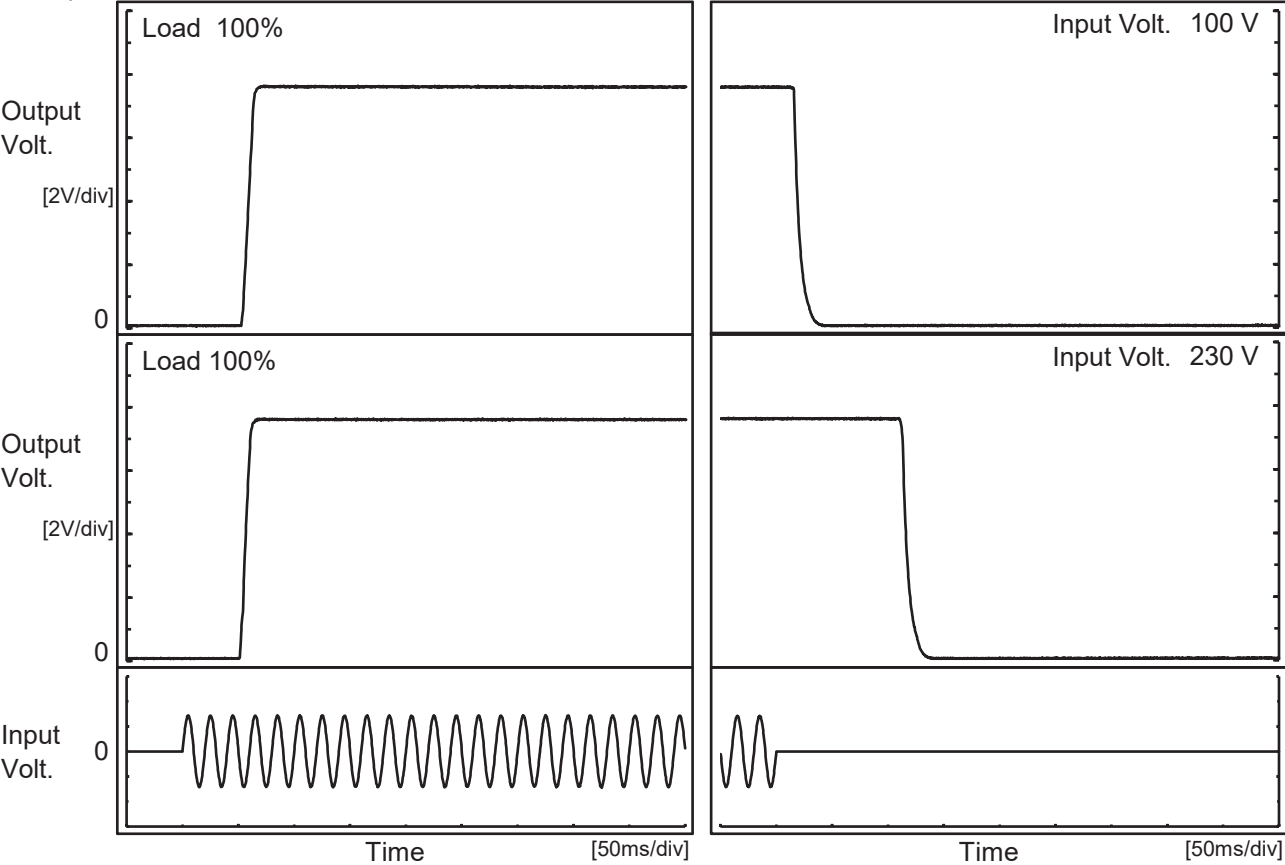
Model		TEPS10F15	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+15V0.7A	





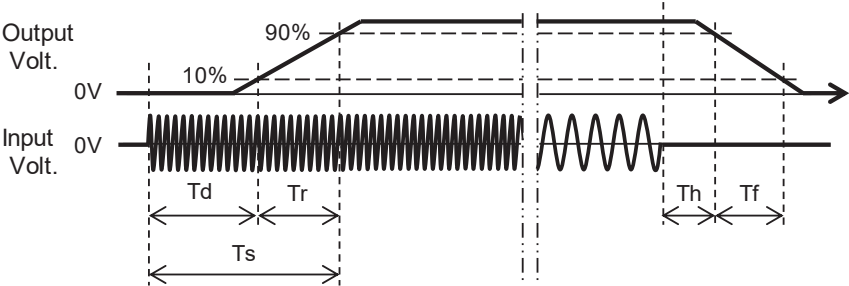
Model		TEPS10F15	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+15V0.7A	

1.Graph



2.Values

		[ms]				
Input Volt.	Time	Td	Tr	Ts	Th	Tf
100V		54.3	8.8	63.1	16.5	11.8
230V		52.0	8.5	60.5	113.5	12.0



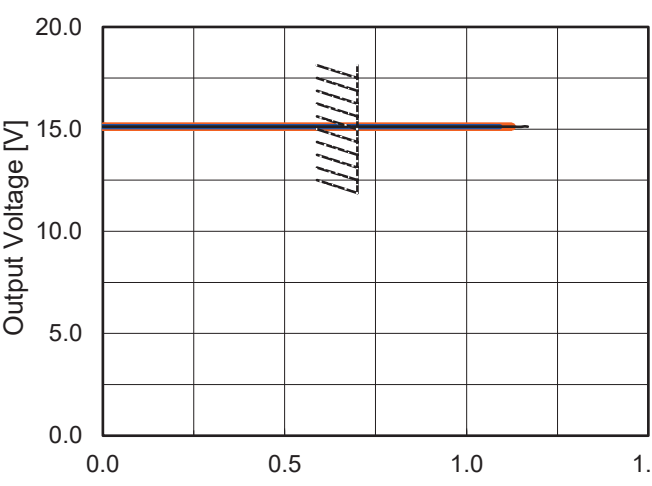
COSEL

Model		TEPS10F15	
Item		Hold-Up Time	
Object		+15V0.7A	
1.Graph		2.Values	

Model		TEPS10F15		Temperature 25°C																																																						
Item		Instantaneous Interruption Compensation		Testing Circuitry Figure A																																																						
Object		+15V0.7A																																																								
1.Graph		<div><div>—△—</div>Input Volt. 100V</div> <div><div>---□---</div>Input Volt. 200V</div> <div><div>---○---</div>Input Volt. 230V</div>		2.Values																																																						
<div><div>Instantaneous Compensation Time [ms]</div><div><div>10000</div><div>1000</div><div>100</div><div>10</div><div>1</div><div>0.0</div><div>0.2</div><div>0.4</div><div>0.6</div><div>0.8</div><div>Load Current [A]</div></div></div>		<table><tr><th rowspan="2">Load Current [A]</th><th colspan="3">Time [ms]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 200[V]</th><th>Input Volt. 230[V]</th></tr><tr><td>0.00</td><td>-</td><td>-</td><td>-</td></tr><tr><td>0.07</td><td>195</td><td>1153</td><td>1455</td></tr><tr><td>0.14</td><td>97</td><td>423</td><td>899</td></tr><tr><td>0.21</td><td>63</td><td>283</td><td>379</td></tr><tr><td>0.28</td><td>46</td><td>212</td><td>284</td></tr><tr><td>0.35</td><td>36</td><td>169</td><td>227</td></tr><tr><td>0.42</td><td>28</td><td>140</td><td>189</td></tr><tr><td>0.49</td><td>24</td><td>119</td><td>162</td></tr><tr><td>0.56</td><td>20</td><td>105</td><td>141</td></tr><tr><td>0.63</td><td>18</td><td>92</td><td>125</td></tr><tr><td>0.70</td><td>15</td><td>81</td><td>110</td></tr><tr><td>0.77</td><td>13</td><td>72</td><td>99</td></tr></table>		Load Current [A]	Time [ms]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.00	-	-	-	0.07	195	1153	1455	0.14	97	423	899	0.21	63	283	379	0.28	46	212	284	0.35	36	169	227	0.42	28	140	189	0.49	24	119	162	0.56	20	105	141	0.63	18	92	125	0.70	15	81	110	0.77	13	72	99
Load Current [A]	Time [ms]																																																									
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]																																																							
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0.77	13	72	99																																																							
Note: Slanted line shows the range of the rated load current.																																																										



Model		TEPS10F15		Temperature Testing Circuitry	25°C Figure A
Item		Overcurrent Protection			
Object		+15V0.7A			
2.Values					
2.Values					

1.Graph		Input Volt. 100V			
		Input Volt. 200V			
		Input Volt. 230V			
Output Voltage [V]		Load Current [A]			
Note: Slanted line shows the range of the rated load current.					
Overcurrent protection is Hiccup mode.					

Output Voltage [V]	Load Current [A]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
15.0	1.17	1.09	1.12
14.5	-	-	-
14.0	-	-	-
13.0	-	-	-
12.0	-	-	-
11.0	-	-	-
10.0	-	-	-
8.0	-	-	-
6.0	-	-	-
4.0	-	-	-
2.0	-	-	-
1.0	-	-	-



Model	TEPS10F15		
Item	Ambient Temperature Drift	Testing Circuitry Figure A	
Object	+15V0.7A		
1.Values Load 100%			
Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	15.108	15.109	15.109
25	15.124	15.125	15.125
60	15.112	15.114	15.114
Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A	
Object	+15V0.7A		
1.Values			
Ambient Temperature[°C]	Input Voltage [V]		
	Load 50%	Load 100%	
-20	38	52	
25	35	49	
60	34	50	
Item	Overvoltage Protection	Testing Circuitry Figure A	
Object	+15V0.7A		
1.Values Load 0%			
Ambient Temperature[°C]	Operating Point [V]		
	Input Volt. 100V	Input Volt. 230V	
-20	18.43	18.40	
25	18.94	18.95	
60	19.37	19.40	

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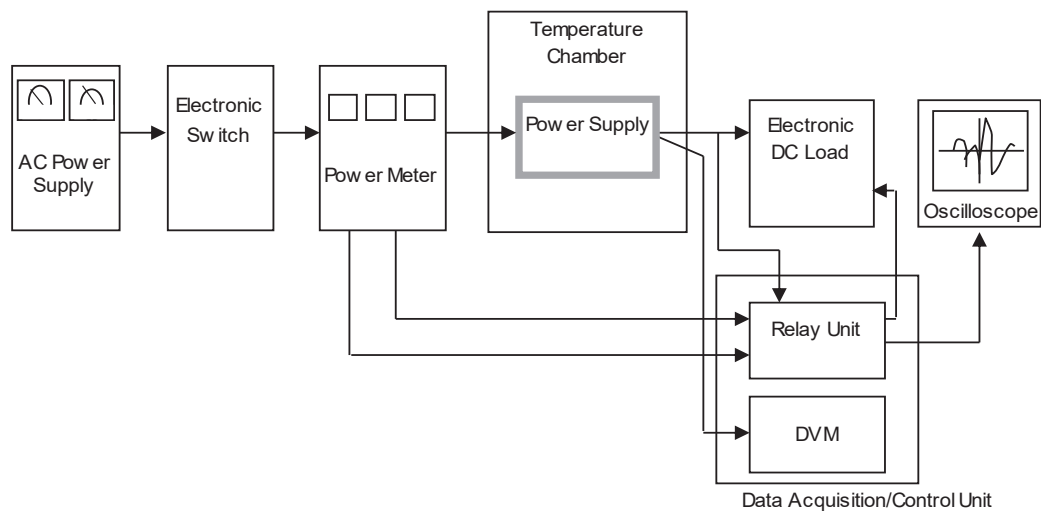


Figure A

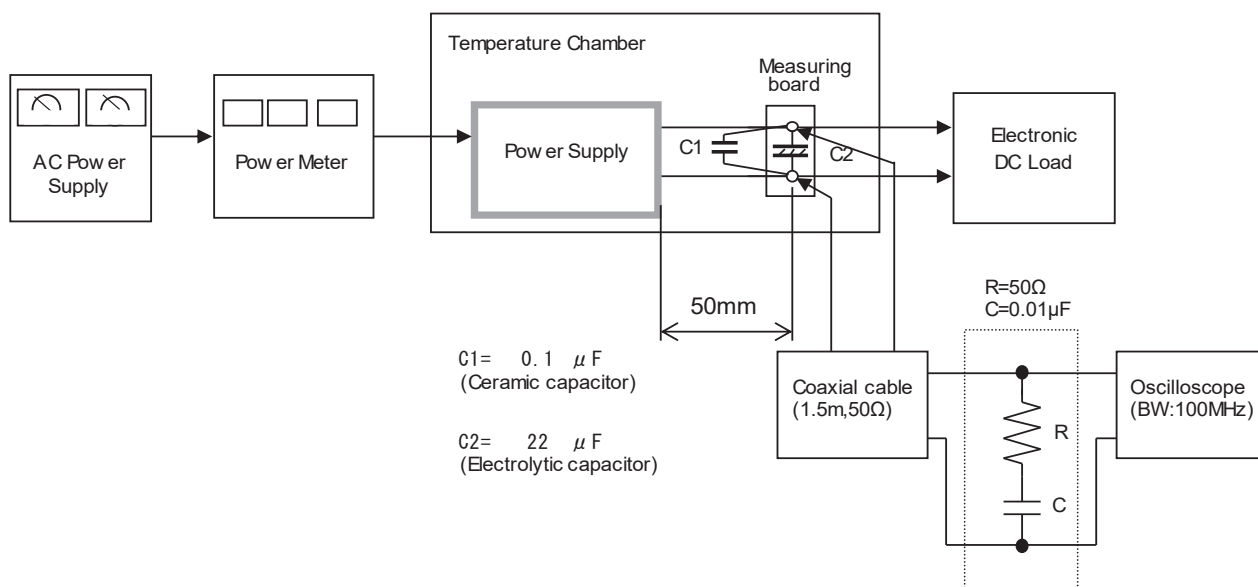


Figure B

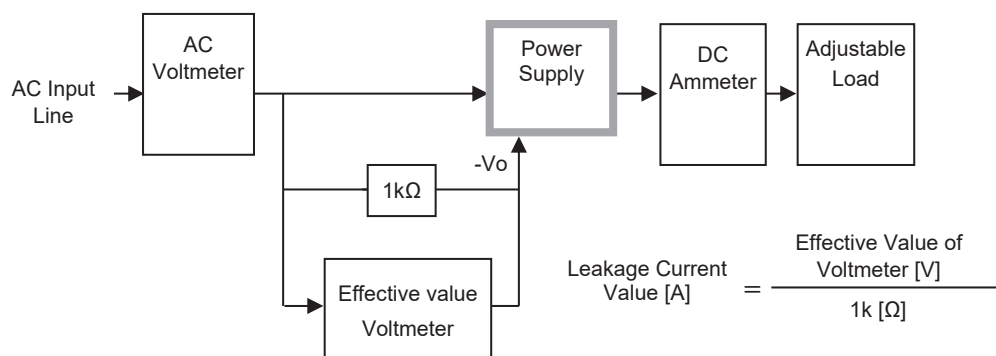


Figure C-1 (DEN-AN)

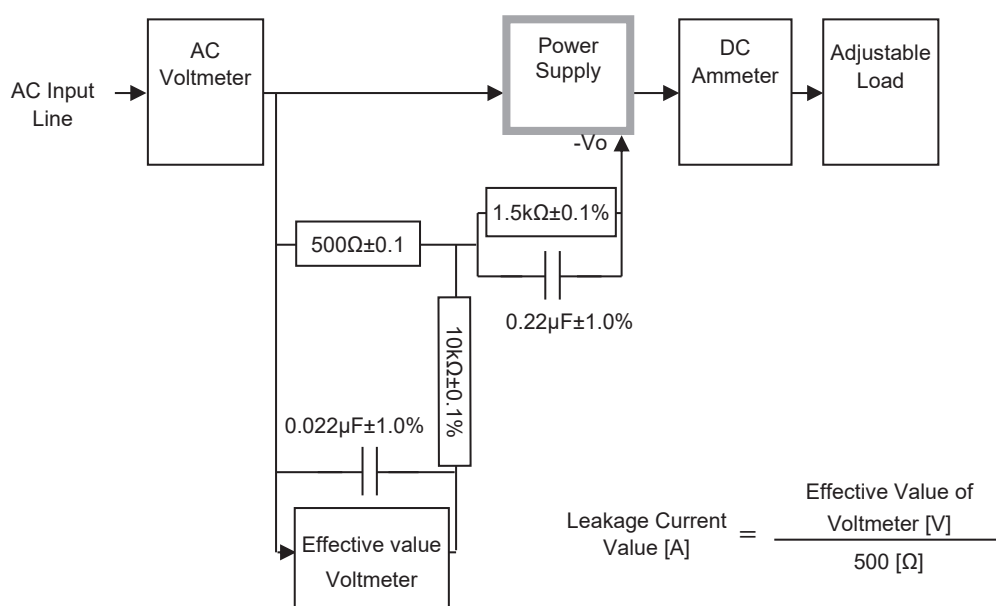


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

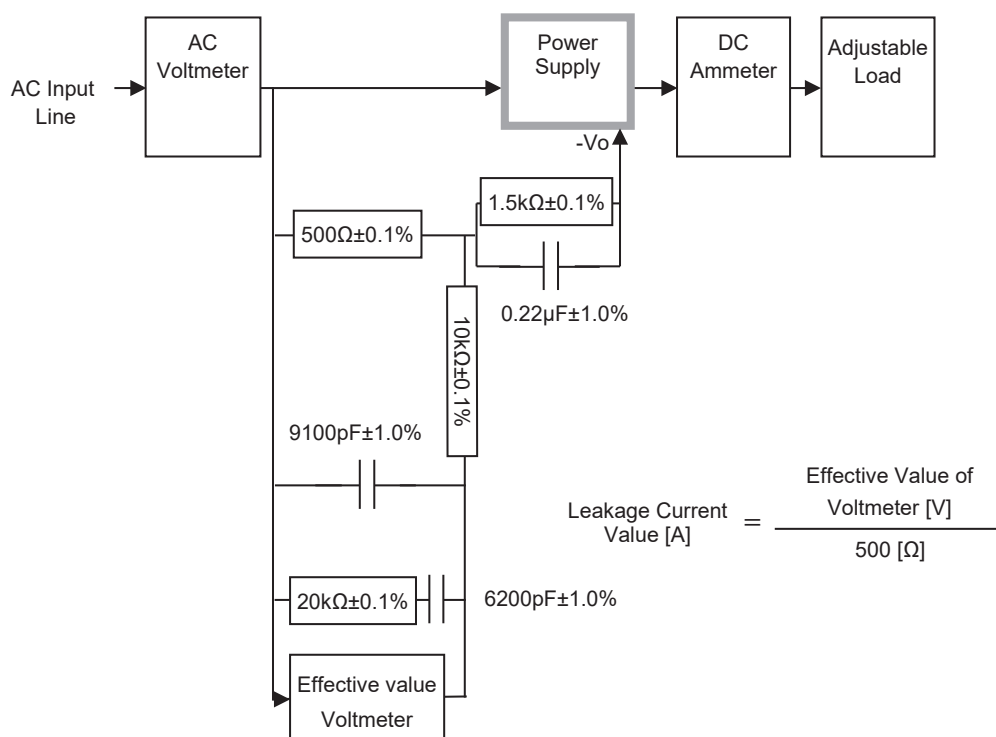


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)