

TEST DATA OF TEPS20F12

Regulated DC Power Supply
February 20, 2025

Approved by : Tetsuro Hirata
Design Manager

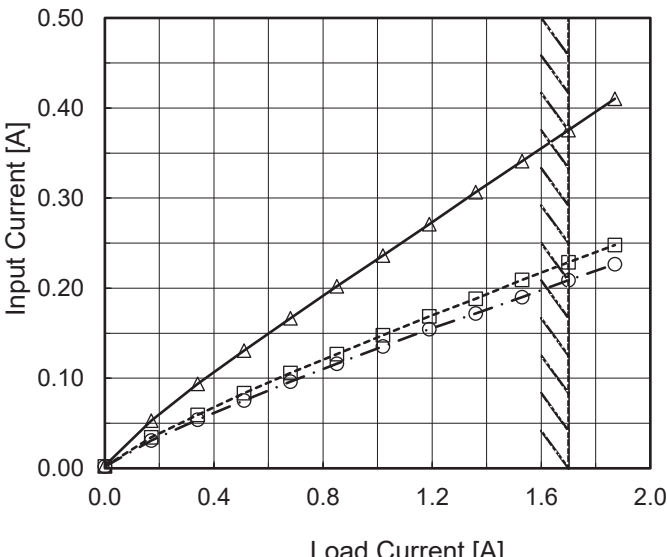
Prepared by : Junichi Otsubo
Design Engineer

COSEL CO.,LTD.

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Model		TEPS20F12		Temperature 25°C																																																						
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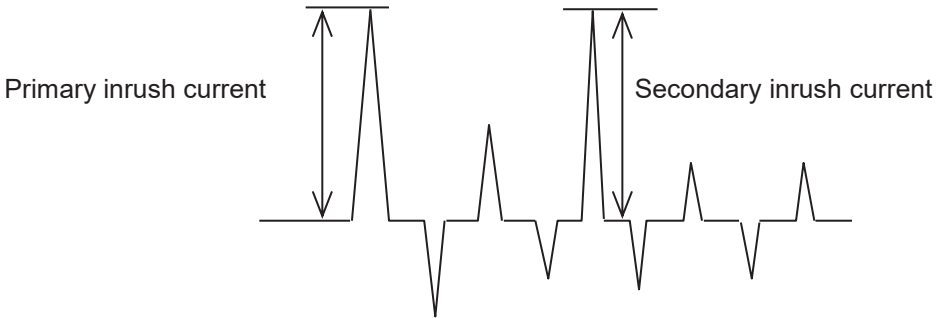
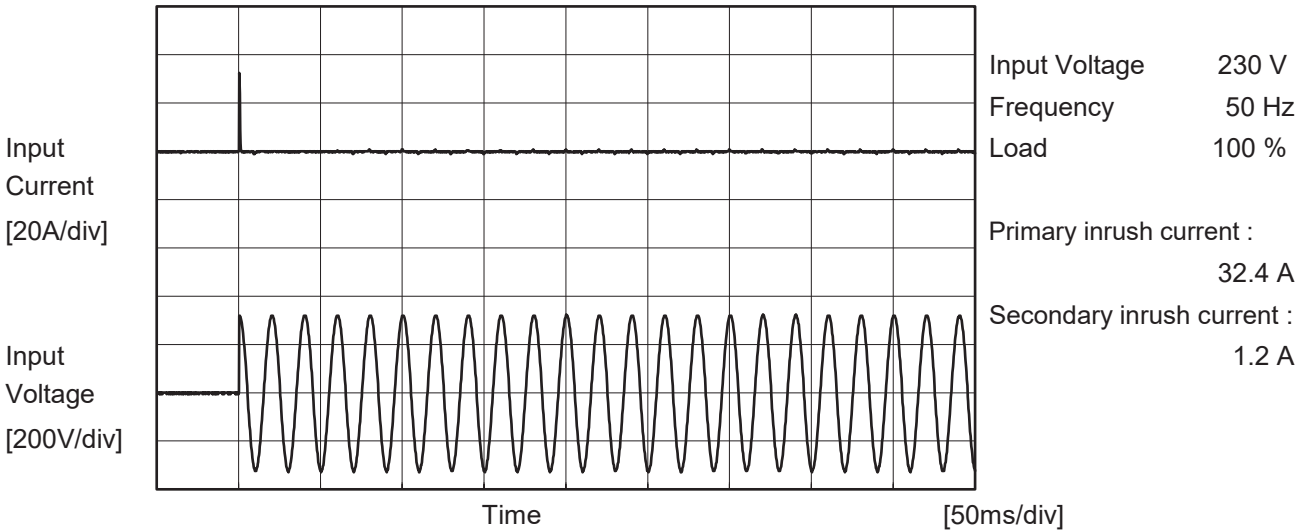
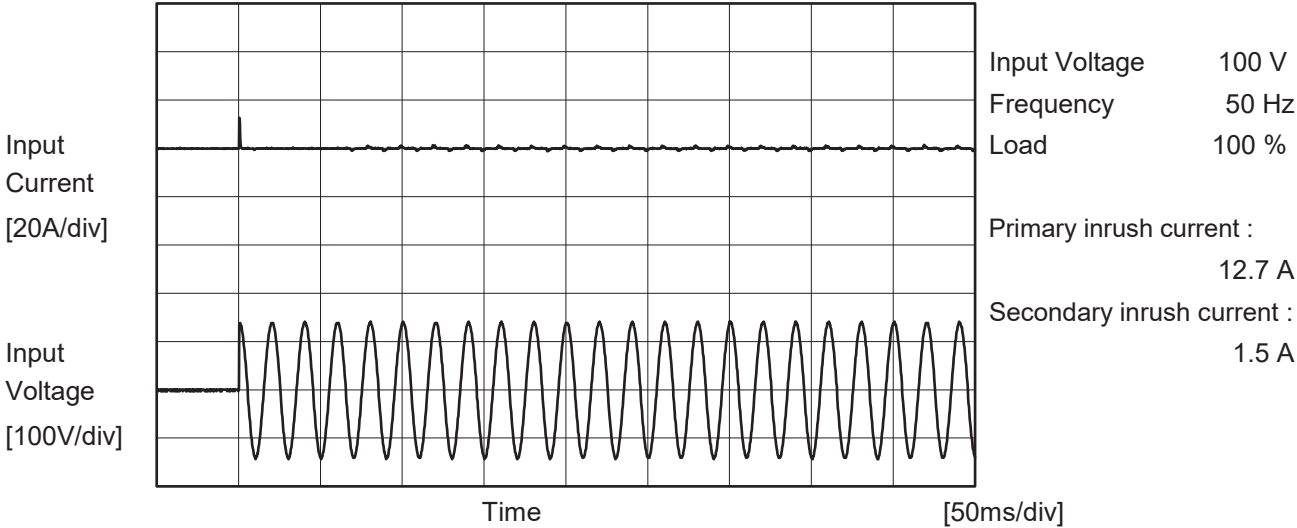
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Model		TEPS20F12	
Item		Inrush Current	Temperature 25°C Testing Circuitry Figure A
Object			





LOREL		Temperature 25°C Testing Circuitry Figure C
Model	TEPS20F12	
Item	Leakage Current	
Object	_____	

1.Results

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	264 [V]	
DEN-AN	Figure C-1	Both phases	23	47	54	Operation
		One of phases	28	70	81	Stand by
IEC62368-1	Figure C-2	Both phases	19	44	52	Operation
		One of phases	28	69	80	Stand by
	Figure C-3	Both phases	19	45	52	Operation
		One of phases	28	69	81	Stand by

The value for "One of phases" is the reference value only.

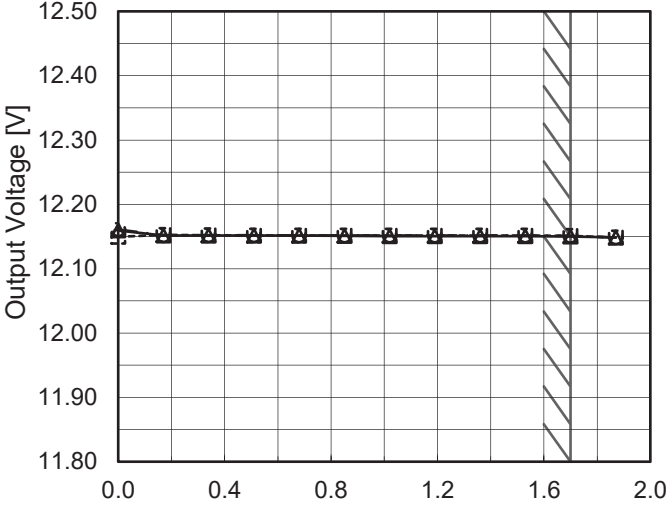
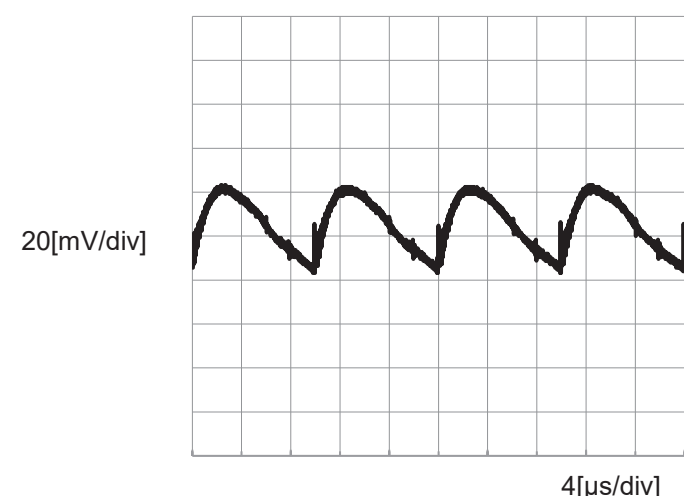
2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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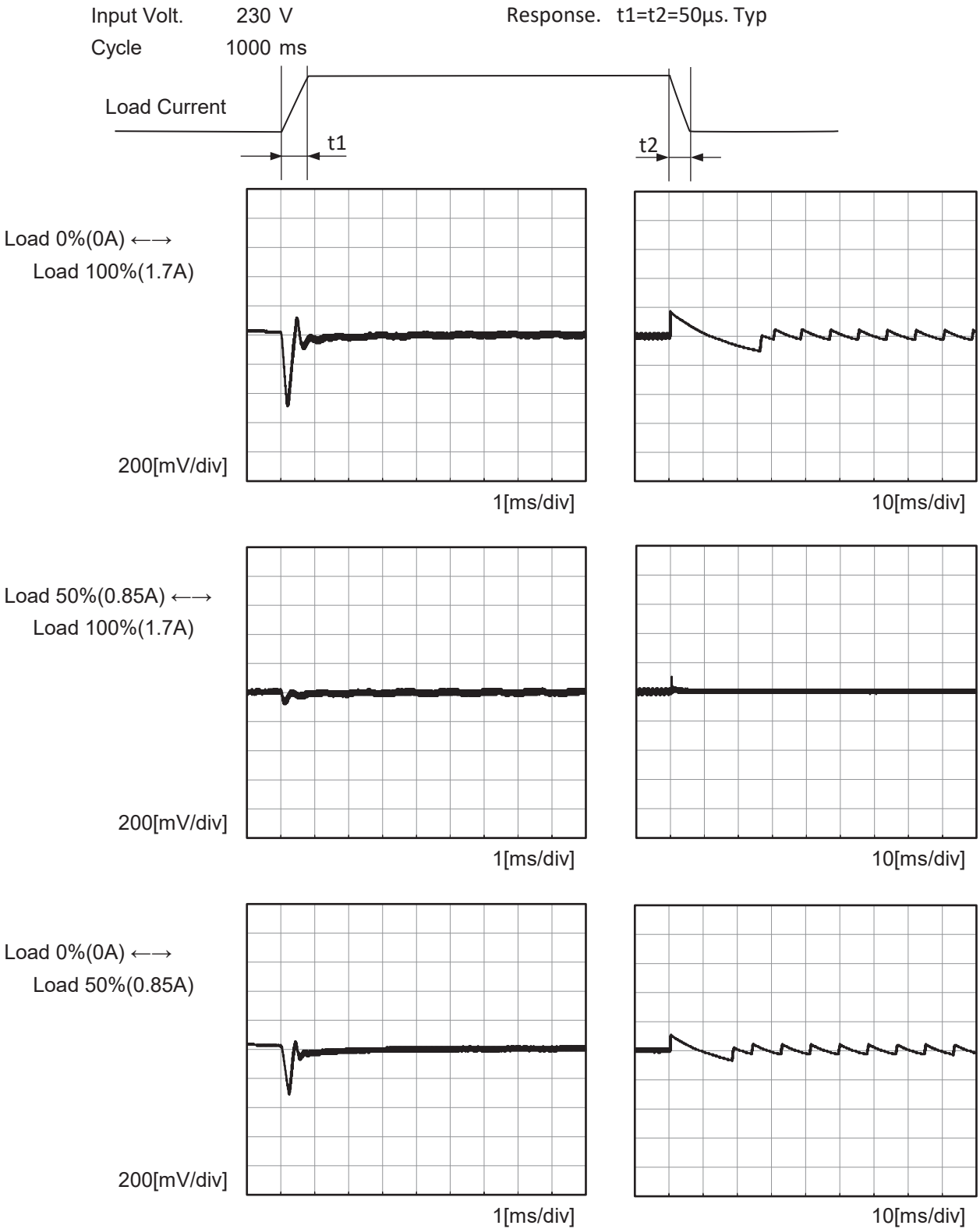
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Item	Ripple-Noise	Temperature	25°C																																																							
Object	+12V1.7A	Testing Circuitry	Figure B																																																							
1.Graph																																																										
<div><div><div>Input Voltage</div><div>230V</div></div><div><div>Load</div><div>100%</div></div><div></div></div>																																																										



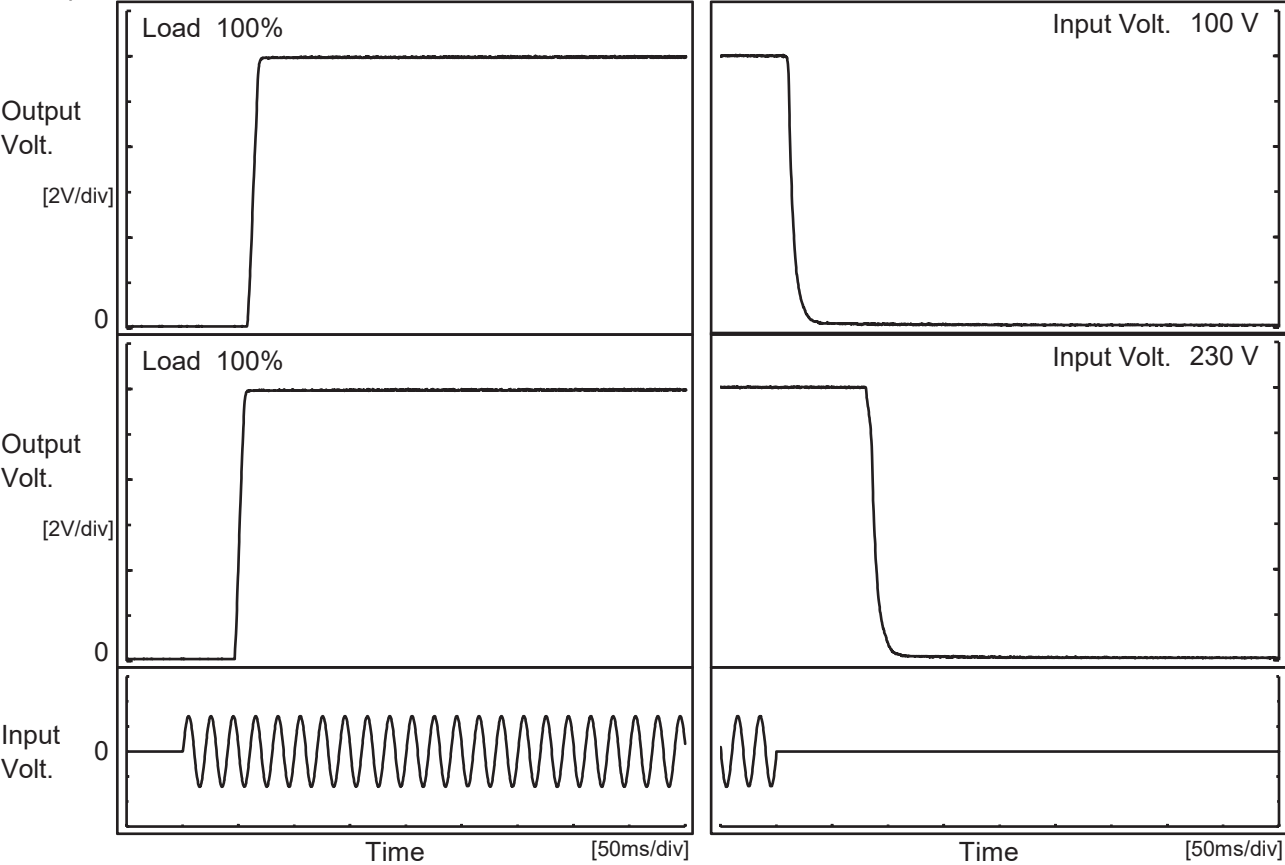
Model		TEPS20F12	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+12V1.7A	





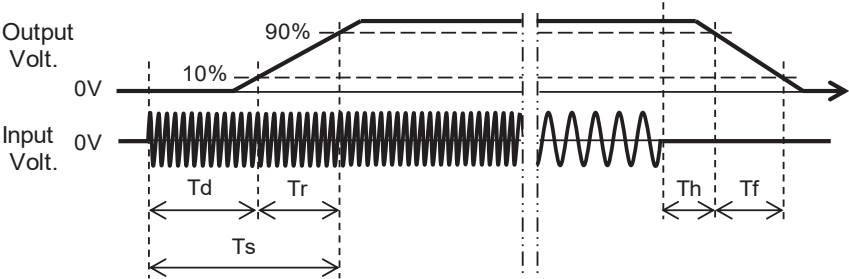
Model		TEPS20F12	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+12V1.7A	

1.Graph



2.Values

		[ms]				
Input Volt.	Time	Td	Tr	Ts	Th	Tf
100V		59.0	8.3	67.3	11.3	10.8
230V		47.8	7.3	55.1	83.0	13.8



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Model		TEPS20F12	
Item		Hold-Up Time	
Object		+12V1.7A	
1.Graph		2.Values	

<

Model		TEPS20F12	Temperature 25°C Testing Circuitry Figure A																																																							
Item		Instantaneous Interruption Compensation																																																								
Object		+12V1.7A																																																								
1.Graph		<div><div>—△—</div>Input Volt. 100V</div> <div><div>---□---</div>Input Volt. 200V</div> <div><div>-·-○-·-</div>Input Volt. 230V</div> <p>Instantaneous Compensation Time [ms]</p> <p>Load Current [A]</p>	2.Values																																																							
		<table><tr><th rowspan="2">Load Current [A]</th><th colspan="3">Time [ms]</th></tr><tr><th>Input Volt. 100[V]</th><th>Input Volt. 200[V]</th><th>Input Volt. 230[V]</th></tr><tr><td>0.00</td><td>-</td><td>-</td><td>-</td></tr><tr><td>0.17</td><td>146</td><td>623</td><td>826</td></tr><tr><td>0.34</td><td>71</td><td>317</td><td>423</td></tr><tr><td>0.51</td><td>46</td><td>210</td><td>283</td></tr><tr><td>0.68</td><td>34</td><td>157</td><td>210</td></tr><tr><td>0.85</td><td>26</td><td>125</td><td>168</td></tr><tr><td>1.02</td><td>21</td><td>104</td><td>140</td></tr><tr><td>1.19</td><td>17</td><td>87</td><td>119</td></tr><tr><td>1.36</td><td>15</td><td>75</td><td>103</td></tr><tr><td>1.53</td><td>12</td><td>65</td><td>89</td></tr><tr><td>1.70</td><td>8</td><td>57</td><td>78</td></tr><tr><td>1.87</td><td>7</td><td>48</td><td>68</td></tr></table>		Load Current [A]	Time [ms]			Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]	0.00	-	-	-	0.17	146	623	826	0.34	71	317	423	0.51	46	210	283	0.68	34	157	210	0.85	26	125	168	1.02	21	104	140	1.19	17	87	119	1.36	15	75	103	1.53	12	65	89	1.70	8	57	78	1.87	7	48	68
Load Current [A]	Time [ms]																																																									
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]																																																							
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1.02	21	104	140																																																							
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1.70	8	57	78																																																							
1.87	7	48	68																																																							
Note: Slanted line shows the range of the rated load current.																																																										



Model		TEPS20F12		Temperature Testing Circuitry	25°C Figure A
Item		Overcurrent Protection			
Object		+12V1.7A			
1.Graph					
		<div><div></div></div>	Input Volt.	100V	
		<div><div></div></div>	Input Volt.	200V	
		<div><div></div></div>	Input Volt.	230V	
Output Voltage [V]					
		Load Current [A]			
Note: Slanted line shows the range of the rated load current.					
Overcurrent protection is Hiccup mode.					

2.Values					
Output Voltage [V]	Load Current [A]				
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]		
12.0	2.68	2.56	2.65		
11.4	-	-	-		
10.8	-	-	-		
9.6	-	-	-		
8.4	-	-	-		
7.2	-	-	-		
6.0	-	-	-		
4.8	-	-	-		
3.6	-	-	-		
2.4	-	-	-		
1.2	-	-	-		
0.0	-	-	-		



		Testing Circuitry Figure A	
Model	TEPS20F12		
Item	Ambient Temperature Drift		
Object	+12V1.7A		
1.Values Load 100%			
Ambient Temperature[°C]		Output Voltage [V]	
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	12.134	12.135	12.135
25	12.141	12.142	12.142
60	12.131	12.133	12.133
Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A	
Object	+12V1.7A		
1.Values			
Ambient Temperature[°C]		Input Voltage [V]	
	Load 50%	Load 100%	
-20	43	62	
25	40	59	
60	38	58	
Item	Overvoltage Protection	Testing Circuitry Figure A	
Object	+12V1.7A		
1.Values Load 0%			
Ambient Temperature[°C]		Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V	
-20	15.61	15.59	
25	15.58	15.58	
60	15.61	15.58	

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BC-12095

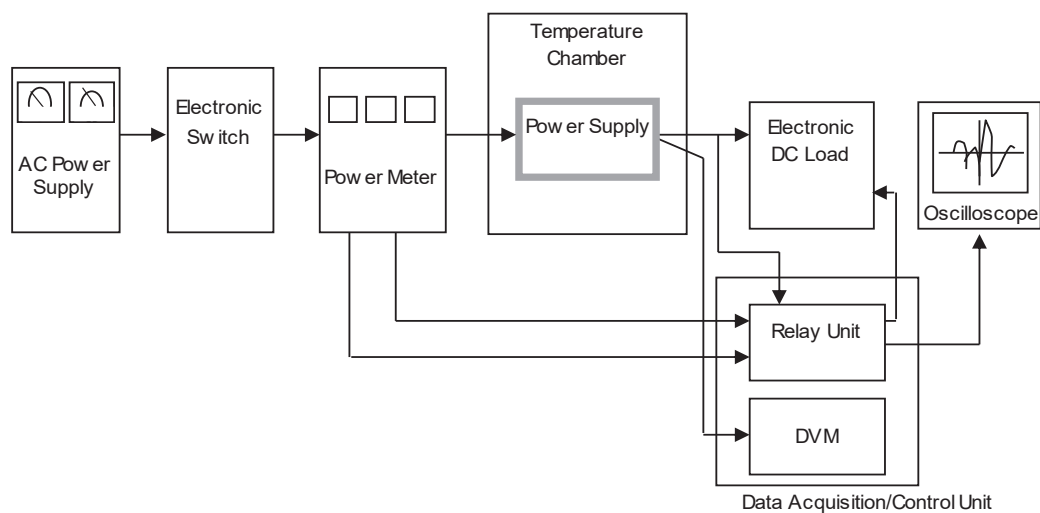


Figure A

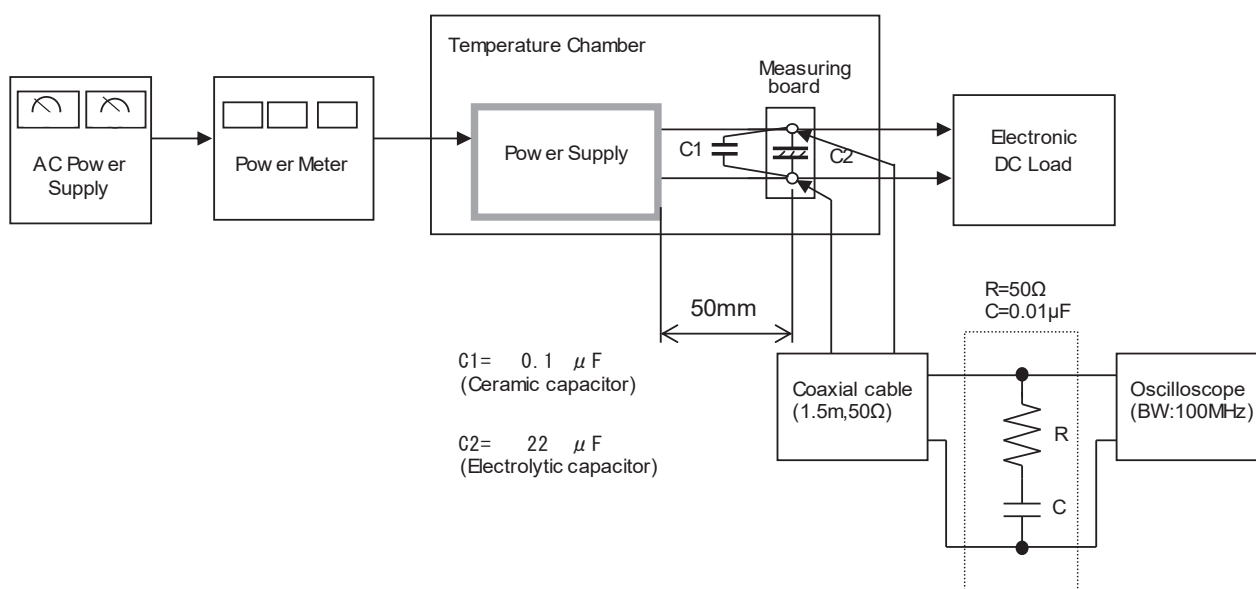


Figure B

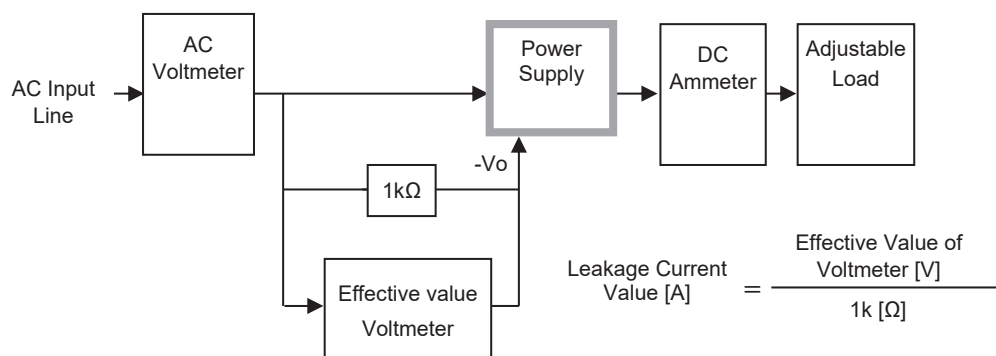


Figure C-1 (DEN-AN)

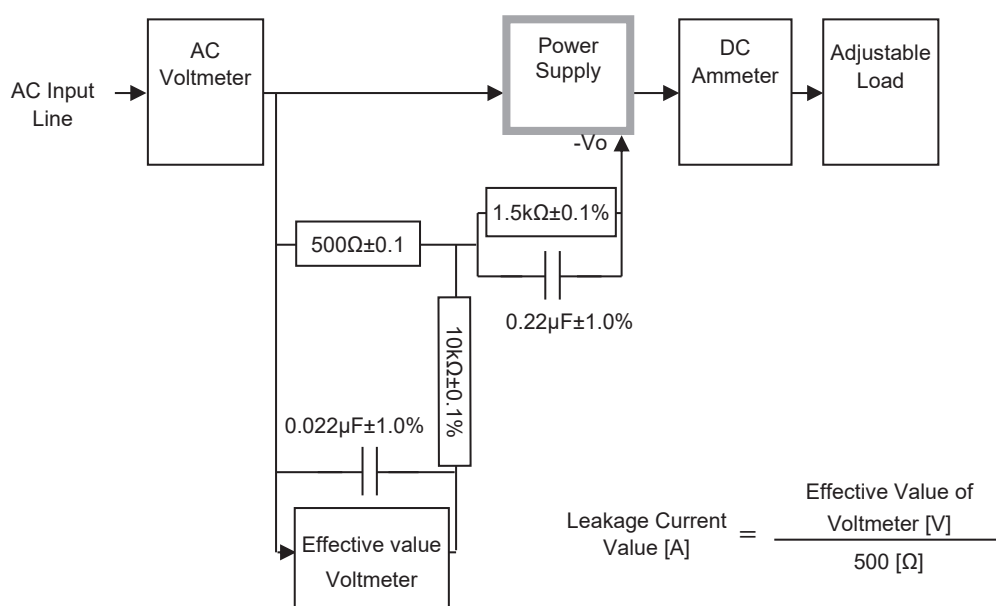


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

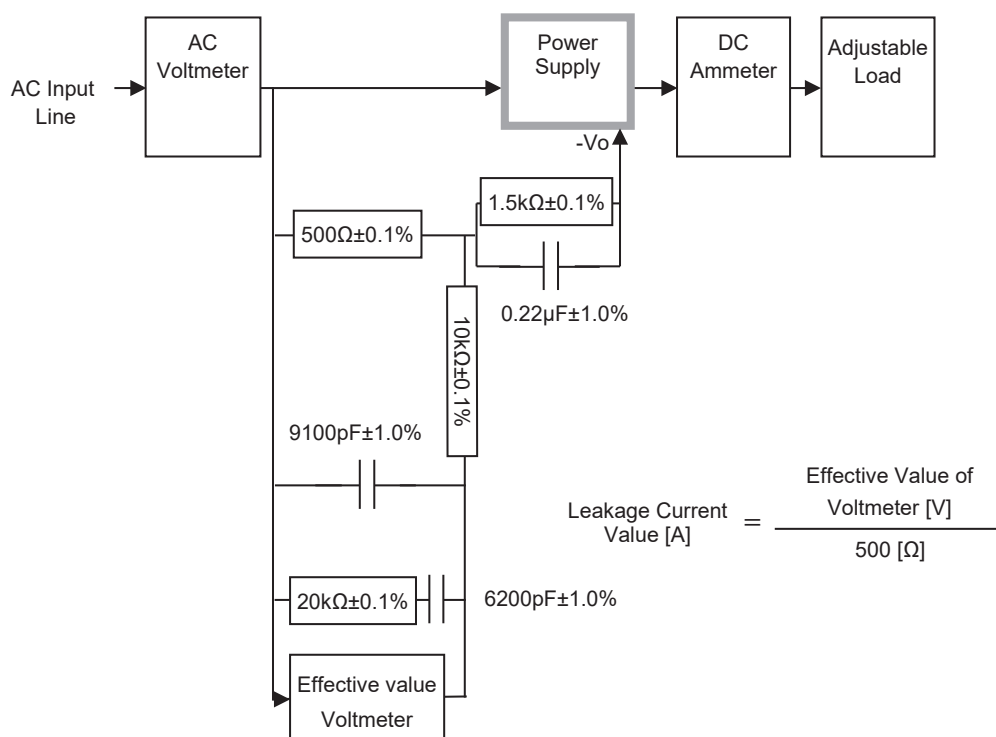


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)