

TEST DATA OF WMA100F-24

Regulated DC Power Supply
October 31, 2023

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Design Manager

Prepared by : Yuya Sakai
Design Engineer

COSEL CO.,LTD.

CONTENTS

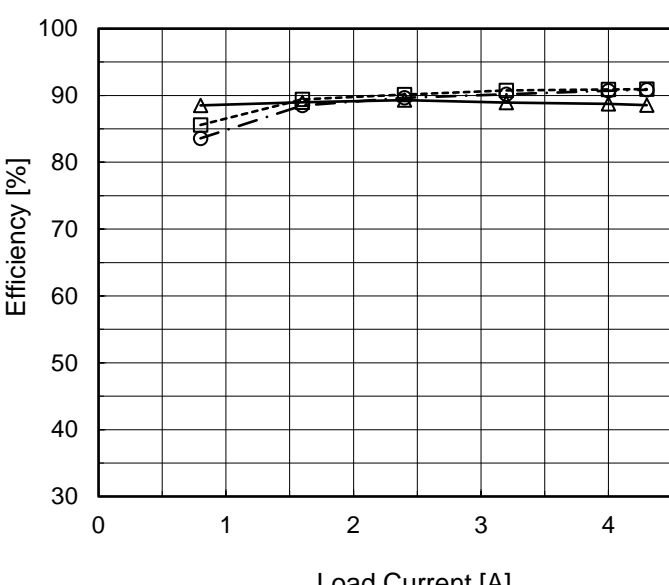
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Model		WMA100F-24	Temperature25°C																																																				
Item		Input Current (by Load Current)	Testing CircuitryFigure A																																																				
Object		+24V4.3A																																																					
1.Graph		<div><div>—△—</div>Input Volt.115V</div> <div><div>- - -□- - -</div>Input Volt.230V</div> <div><div>- · -○- · -</div>Input Volt.264V</div> <div><div><div>Input Current [A]</div><div>2.0</div><div>1.5</div><div>1.0</div><div>0.5</div><div>0.0</div><div>0</div><div>1</div><div>2</div><div>3</div><div>4</div><div>Load Current [A]</div></div></div>	2.Values																																																				
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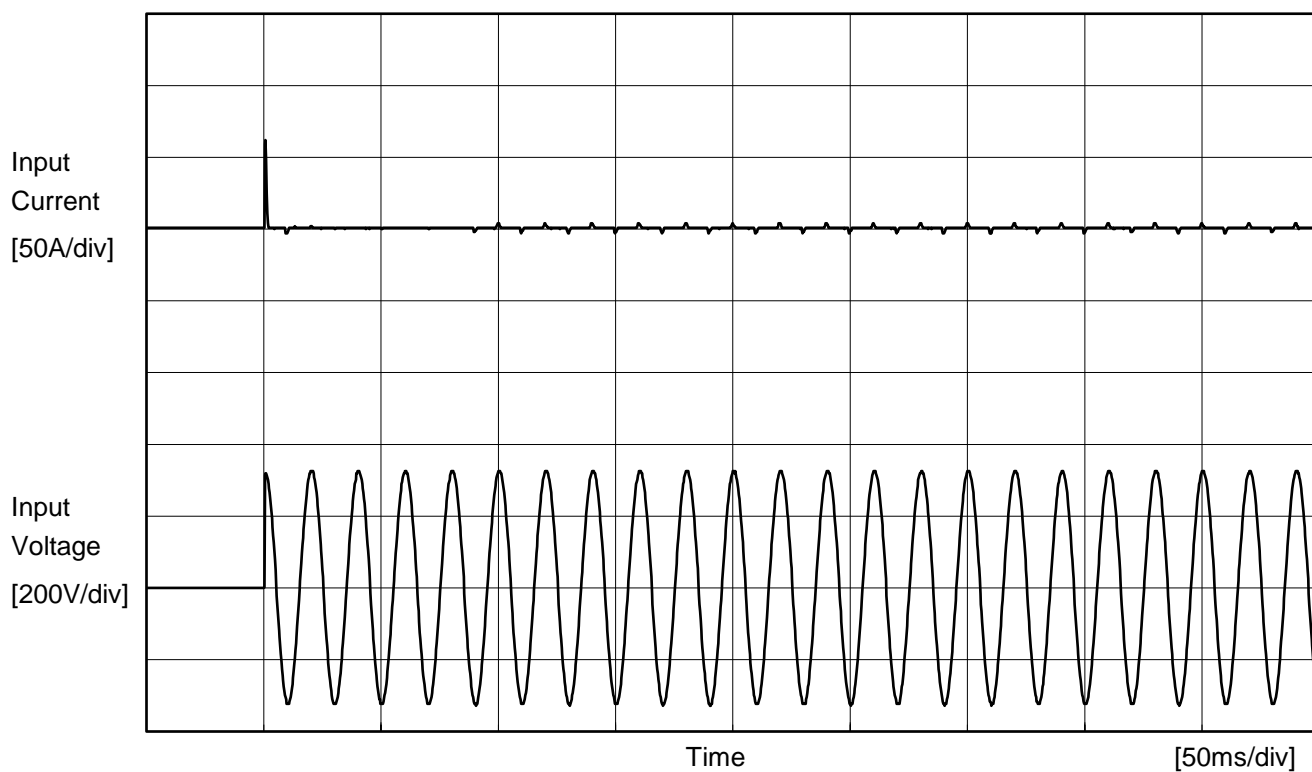


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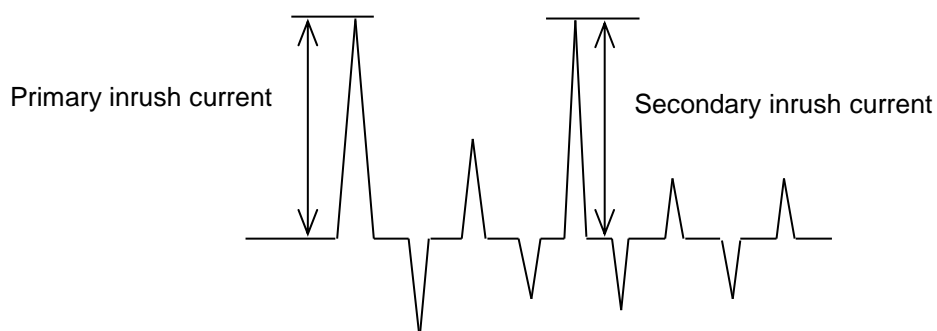
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Model		WMA100F-24	Temperature 25°C Testing Circuitry Figure A
Item		Inrush Current	
Object		+24V4.3A	



Input Voltage	230 V
Frequency	50 Hz
Load	100 %

Primary inrush current	61.6 A
Secondary inrush current	3.6 A



Model		WMA100F-24	Temperature 25°C Testing Circuitry Figure C
Item		Leakage Current	
Object		+24V4.3A	

1.Results

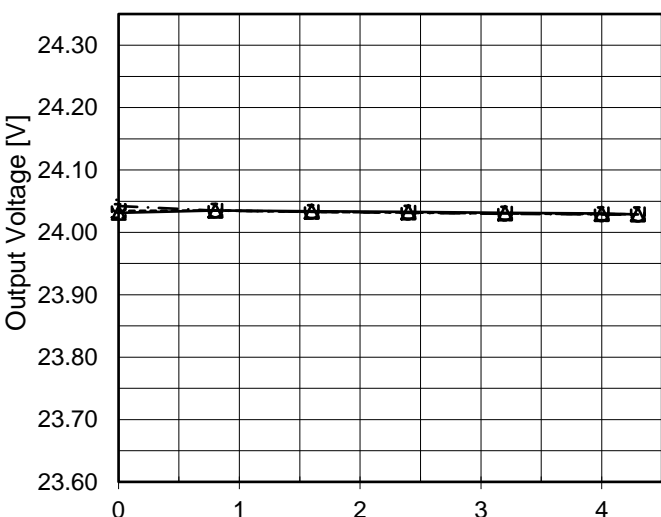
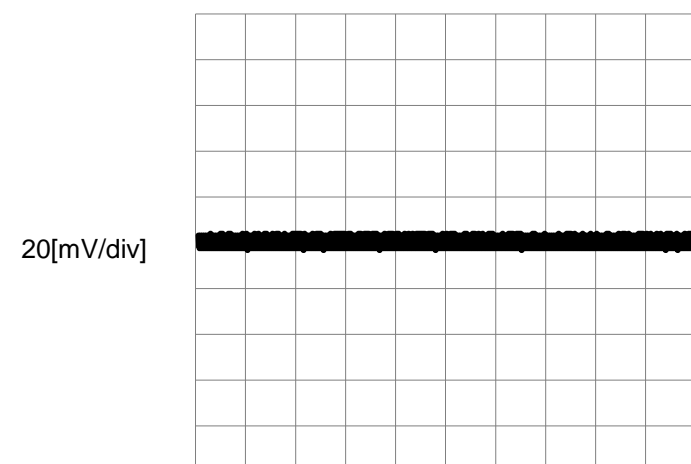
Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			115 [V]	240 [V]	264 [V]	
IEC60601-1	Figure C-1	Both phases	0.14	0.31	0.34	Operation
		One of phases	0.22	0.50	0.56	Stand by
IEC62368-1	Figure C-2	Both phases	0.14	0.32	0.35	Operation
		One of phases	0.22	0.51	0.57	Stand by
	Figure C-3	Both phases	0.14	0.31	0.35	Operation
		One of phases	0.22	0.50	0.56	Stand by

The value for "One of phases" is the reference value only.

2.Condition

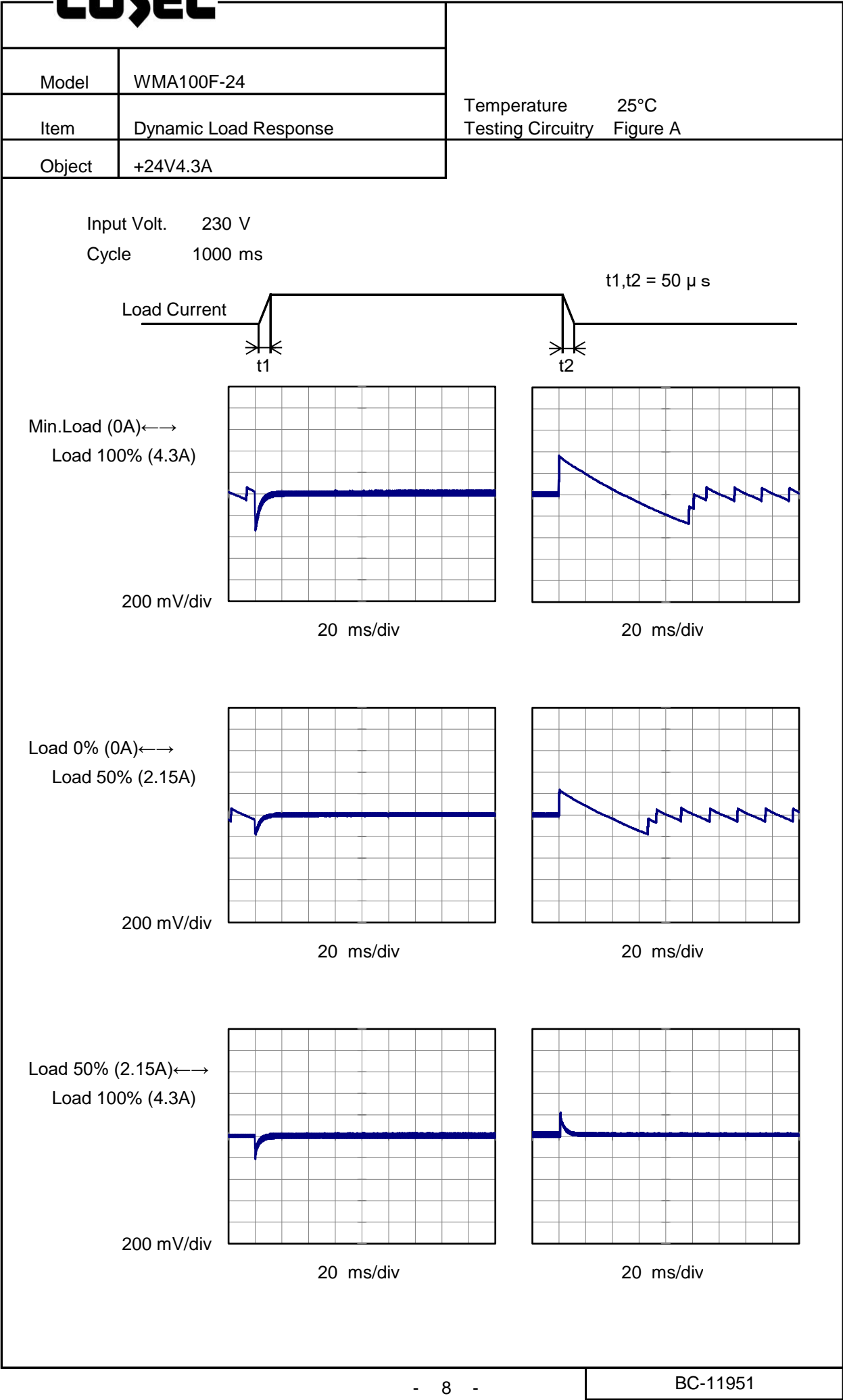
Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.



<div>ModelWMA100F-24</div> <div>ItemLoad Regulation</div> <div>Object+24V4.3A</div>		<div>Temperature25°C</div> <div>Testing CircuitryFigure A</div>																																																				
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<div>1.Graph<div><div>Input Voltage230V</div><div>Load100%</div></div><div><div>20[mV/div]</div><div></div><div>20[μs/div]</div></div></div>																																																						

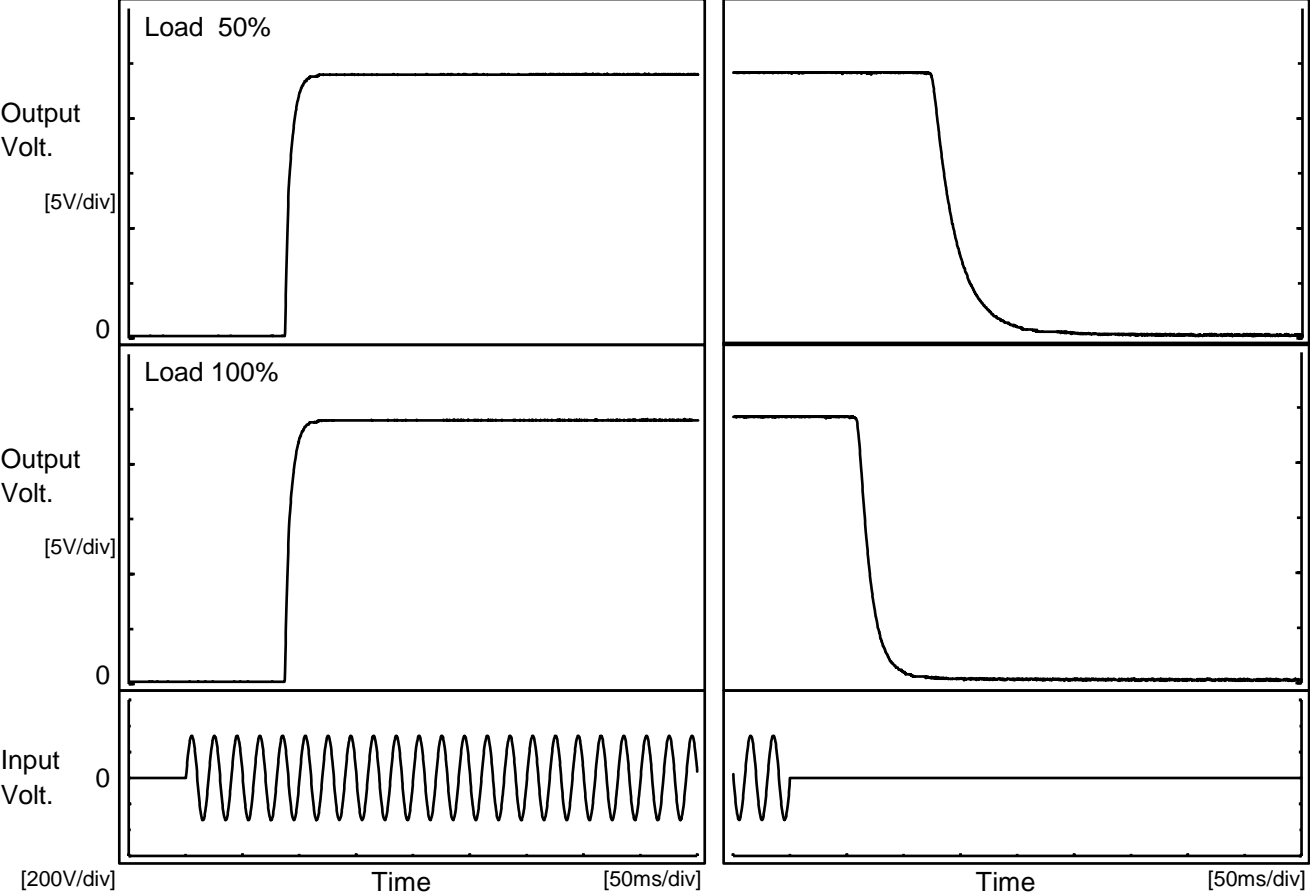
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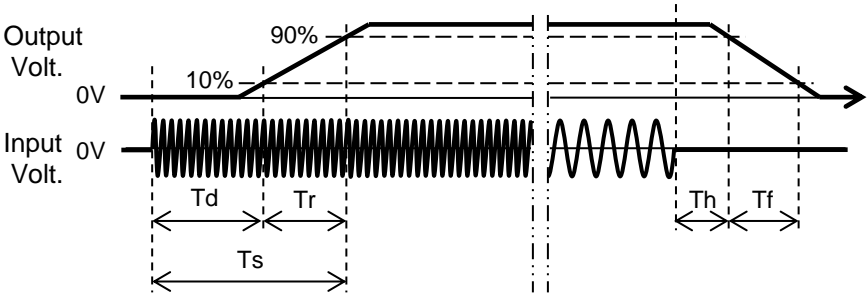
Model		WMA100F-24	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+24V4.3A	

1.Graph



2.Values

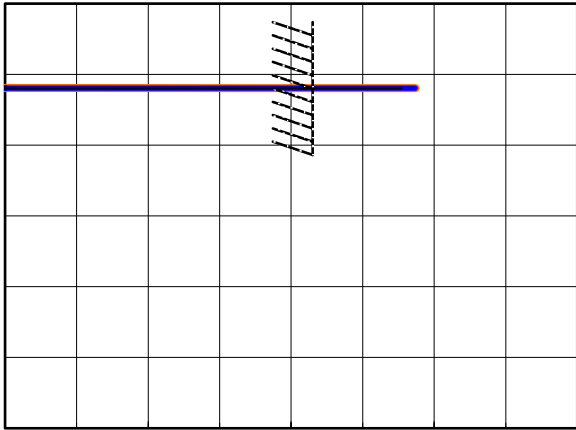
		[ms]				
Load	Time	Td	Tr	Ts	Th	Tf
50 %		87.8	11.3	99.1	127.3	46.0
100 %		87.8	11.0	98.8	60.8	24.3





Model		WMA100F-24	Temperature25°C Testing CircuitryFigure A
Item		Hold-Up Time	
Object		+24V4.3A	
1.Graph			2.Values
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Model		WMA100F-24		Temperature25°C Testing CircuitryFigure A
Item		Overcurrent Protection		
Object		+24V4.3A		
1.Graph				
		Input Volt.	115V	2.Values
		Input Volt.	230V	
		Input Volt.	264V	
Output Voltage [V]				
Load Current [A]				
Note: Slanted line shows the range of the rated load current.				

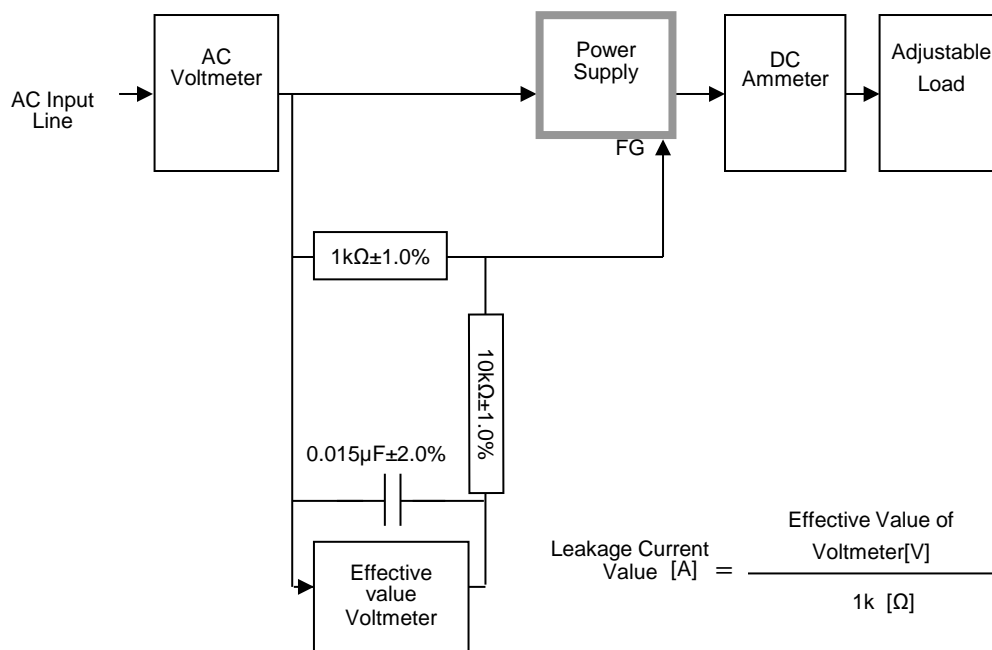
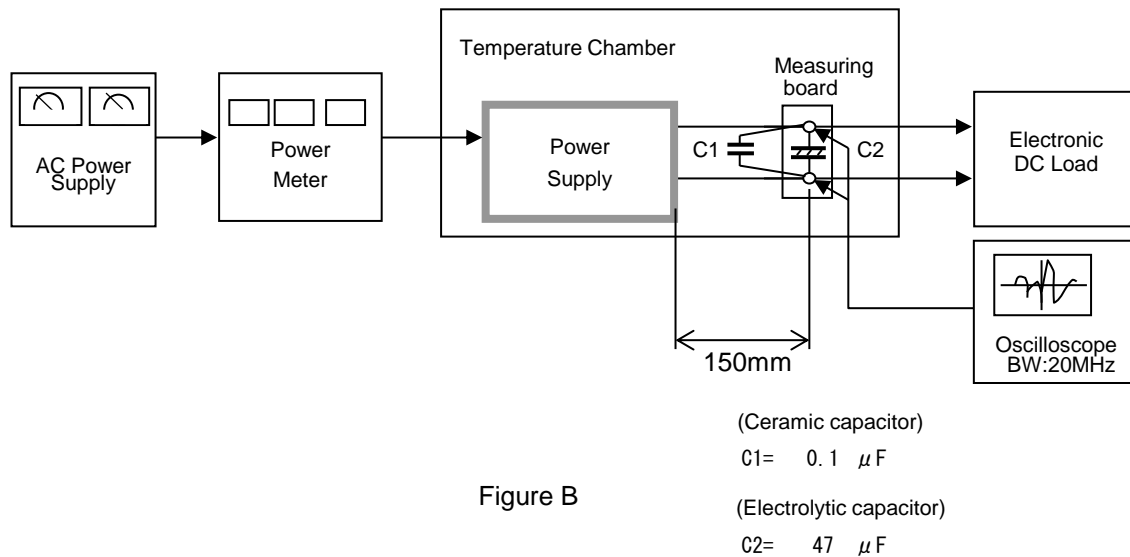
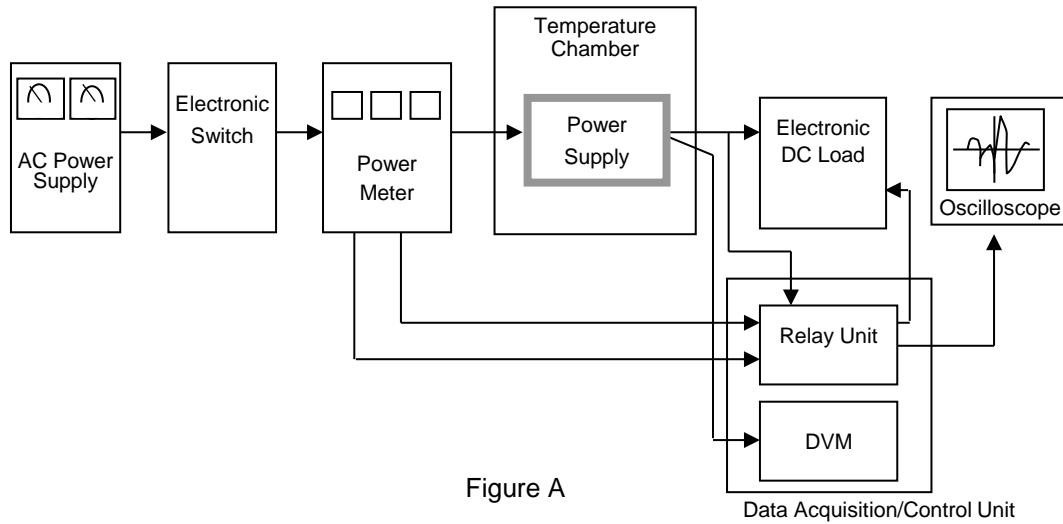
Output Voltage [V]	Load Current [A]		
	Input Volt. 115[V]	Input Volt. 230[V]	Input Volt. 264[V]
24	5.54	5.73	5.74
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-
--	-	-	-



		Testing Circuitry Figure A	
Model	WMA100F-24		
Item	Ambient Temperature Drift		
Object	+24V4.3A		
1.Values Load 100%			
Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 115V	Input Volt. 230V	Input Volt. 264V
-20	24.010	24.012	24.012
25	24.014	24.014	24.013
50	24.010	24.009	24.008
Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A	
Object	+24V4.3A		
1.Values			
Ambient Temperature[°C]	Input Voltage [V]		
	Load 50%	Load 100%	
-20	46	78	
25	45	78	
50	46	79	
Item	Overvoltage Protection	Testing Circuitry Figure A	
Object	+24V4.3A		
1.Values Load 0%			
Ambient Temperature[°C]	Operating Point [V]		
	Input Volt. 115V	Input Volt. 264V	
-20	30.32	30.35	
25	31.41	31.34	
50	32.01	31.98	

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BC-11951



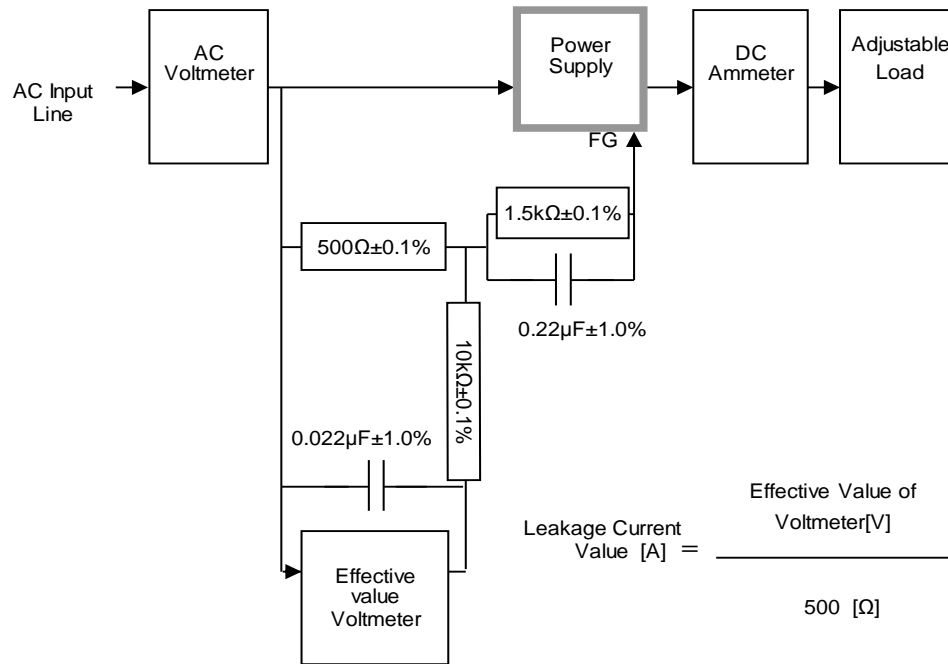


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

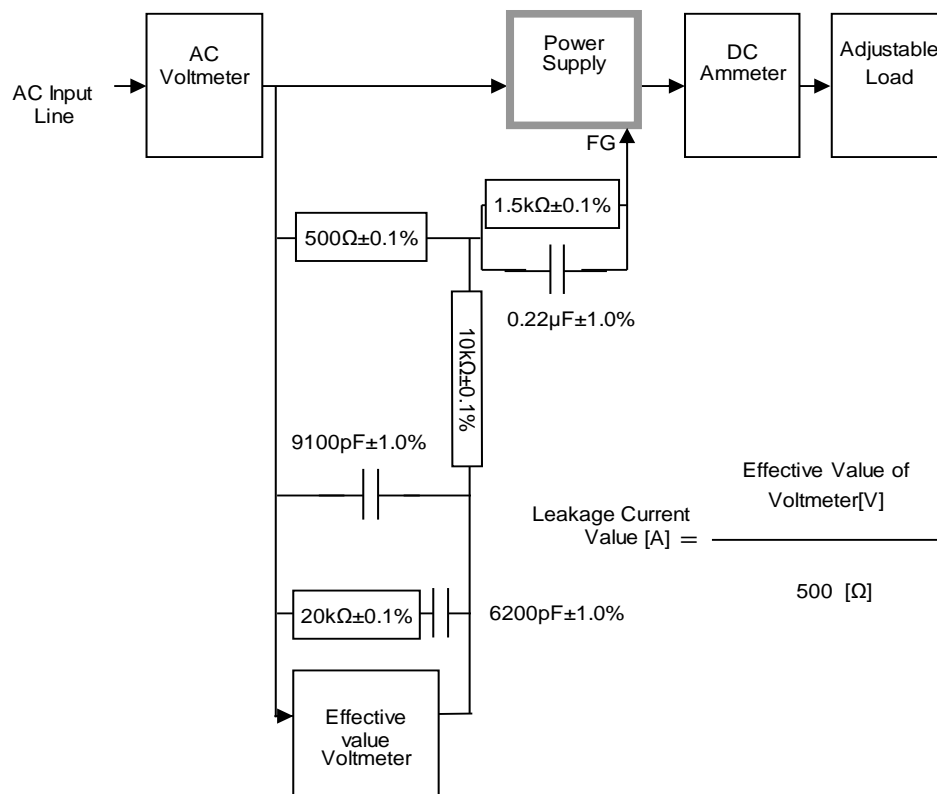


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)