参考資料

COSEL

TEST DATA OF YS505A
(100V INPUT)

Regulated DC Power Supply

Sep. 22, 1999

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コーセル株式会社

COSEL CO., LTD.

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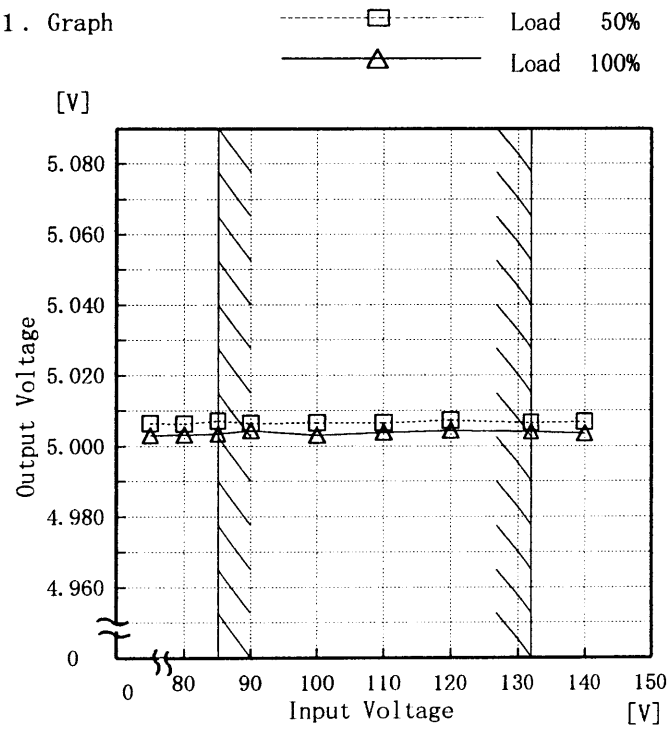
Model	YS505A
Item	Line Regulation 静的入力変動
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
75	5.007	5.003
80	5.006	5.003
85	5.007	5.004
90	5.006	5.005
100	5.007	5.003
110	5.007	5.004
120	5.007	5.004
132	5.007	5.004
140	5.007	5.004

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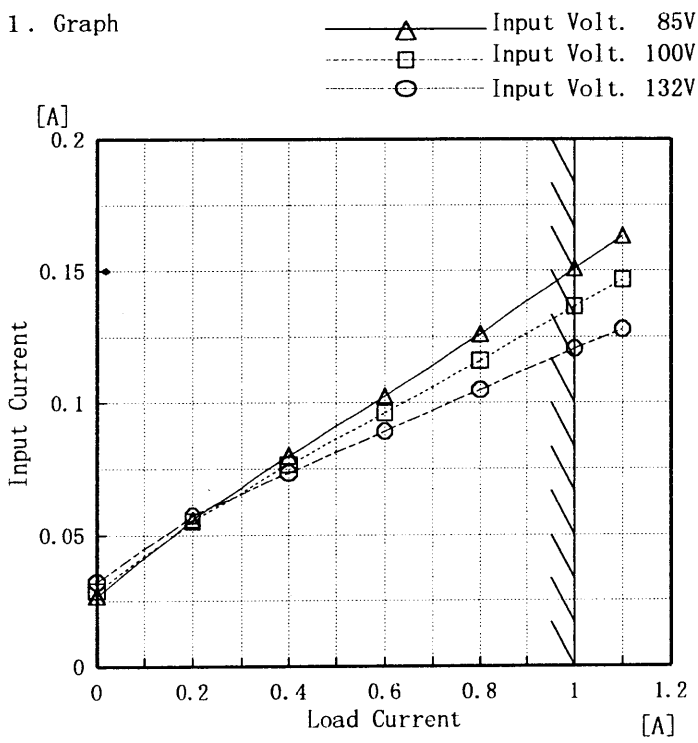
COSEL

Model	YS505A
Item	Input Current (by Load Current) 入力電流（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

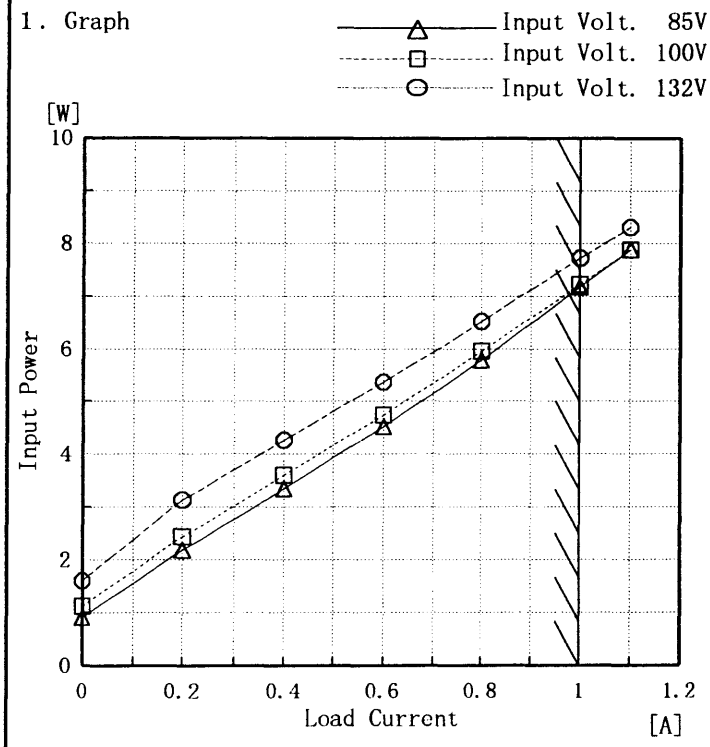
Load Current [A]	Input Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.027	0.029	0.032
0.2	0.056	0.055	0.057
0.4	0.080	0.077	0.074
0.6	0.103	0.096	0.089
0.8	0.126	0.116	0.105
1.0	0.151	0.136	0.121
1.1	0.163	0.147	0.128
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	YS505A
Item	Input Power (by Load Current) 入力電力（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

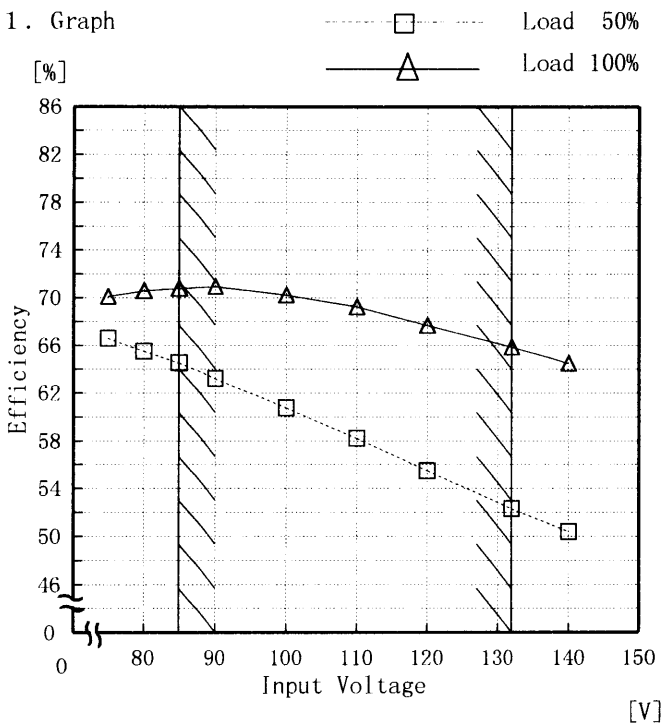
Load Current [A]	Input Power [W] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.91	1.12	1.60
0.2	2.18	2.44	3.14
0.4	3.35	3.60	4.27
0.6	4.52	4.74	5.37
0.8	5.79	5.96	6.52
1.0	7.18	7.23	7.73
1.1	7.89	7.88	8.31
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	YS505A
Item	Efficiency (by Input Voltage) 効率（入力電圧特性）
Object	――――――――――

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
75	66.6	70.1
80	65.5	70.6
85	64.5	70.8
90	63.2	70.9
100	60.8	70.2
110	58.2	69.2
120	55.5	67.7
132	52.3	65.8
140	50.4	64.5

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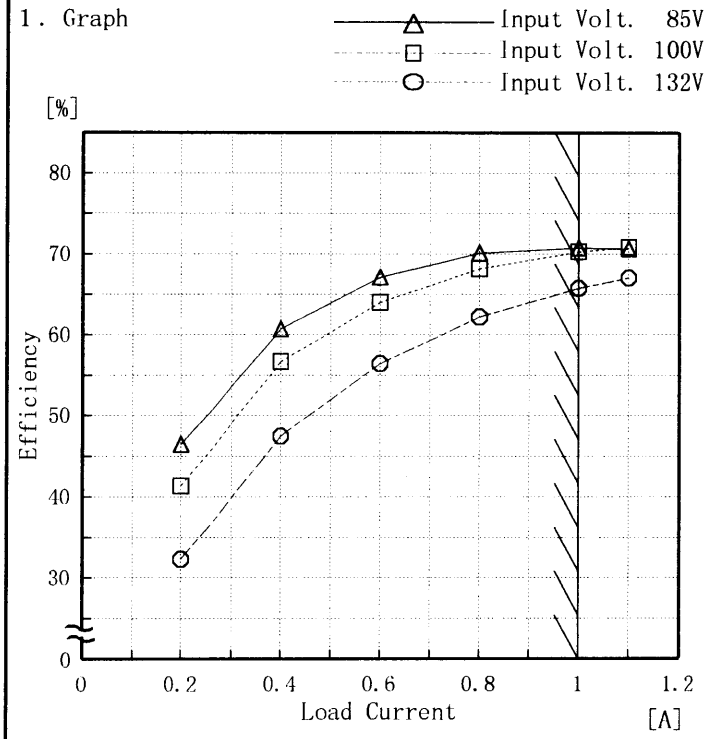
Model	YS505A
Item	Efficiency (by Load Current) 効率（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Efficiency [%] Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.2	46.5	41.4	32.3
0.4	60.7	56.7	47.5
0.6	67.1	64.0	56.4
0.8	70.1	68.1	62.2
1.0	70.8	70.3	65.7
1.1	70.7	70.8	67.1
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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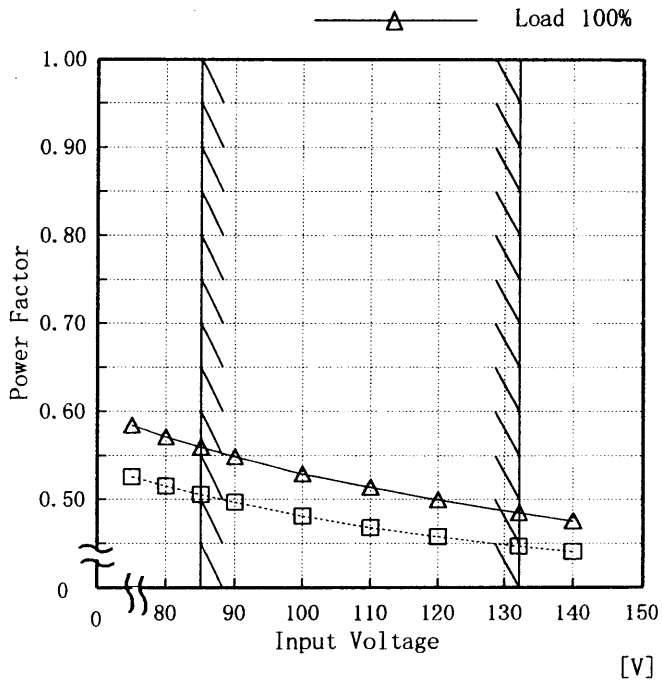
COSEL

Model	YS505A
Item	Power Factor (by Input Voltage) 力率（入力電圧特性）
Object	――――――――――――――

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Power Factor	
	Load 50%	Load 100%
75	0.53	0.58
80	0.52	0.57
85	0.51	0.56
90	0.50	0.55
100	0.48	0.53
110	0.47	0.51
120	0.46	0.50
132	0.45	0.48
140	0.44	0.48

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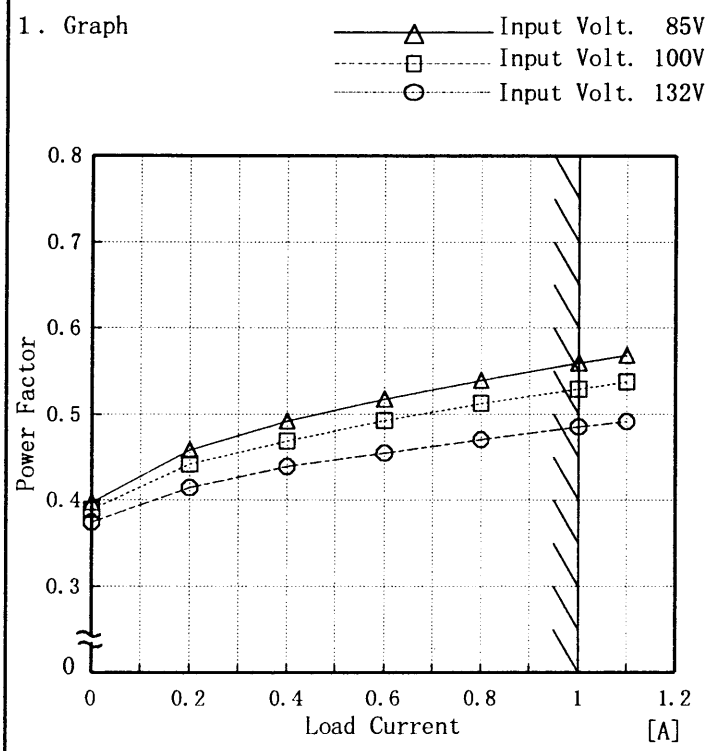
Model	YS505A
Item	Power Factor (by Load Current) 力率（負荷特性）
Output	————

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Power Factor Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	0.40	0.39	0.37
0.2	0.46	0.44	0.41
0.4	0.49	0.47	0.44
0.6	0.52	0.49	0.45
0.8	0.54	0.51	0.47
1.0	0.56	0.53	0.49
1.1	0.57	0.54	0.49
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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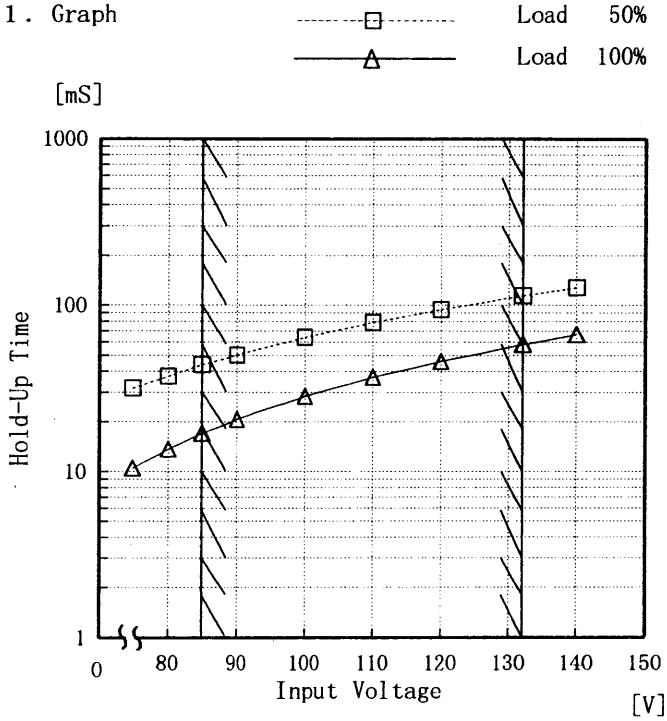
Model	YS505A
Item	Hold-Up Time 出力保持時間
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.

Note: Slanted line shows the range of the rated input voltage.

出力保持時間とは、入力電圧断から出力電圧が、定電圧精度の規格範囲を保持しているところまでの時間。

(注)斜線は定格入力電圧範囲を示す。

2. Values

Input Voltage [V]	Hold-Up Time [mS]	
	Load 50%	Load 100%
75	32	10
80	38	14
85	44	17
90	50	21
100	64	28
110	79	37
120	95	46
132	114	58
140	128	67

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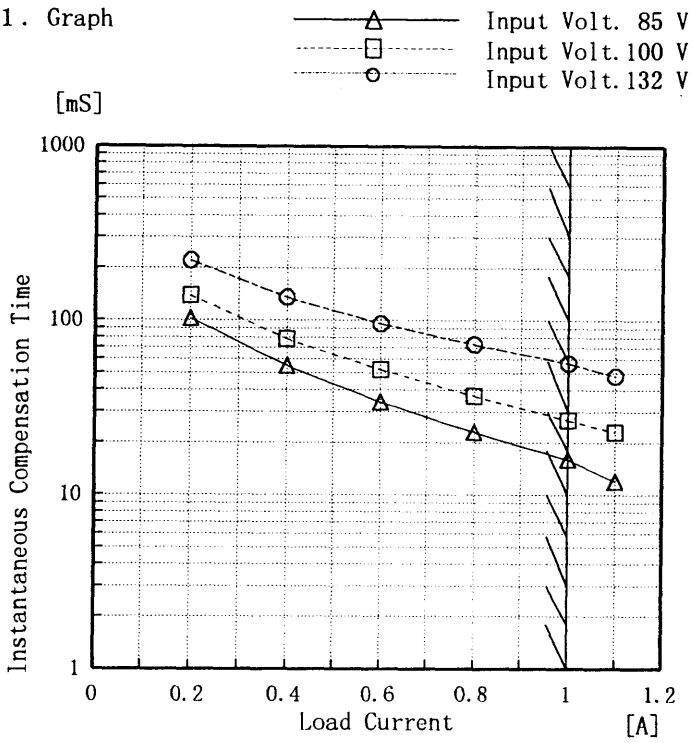
Model	YS505A
Item	Instantaneous Interruption Compensation 瞬時停電保障
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note:Slanted line shows the range of the rated load current.

瞬時停電保障時間とは、出力電圧が定電圧精度の規格範囲を保持している瞬時停電時間をいう。
(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Time [mS]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	—	—	—
0.2	103	139	221
0.4	55	78	136
0.6	34	52	96
0.8	23	37	73
1.0	16	27	57
1.1	12	23	48
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

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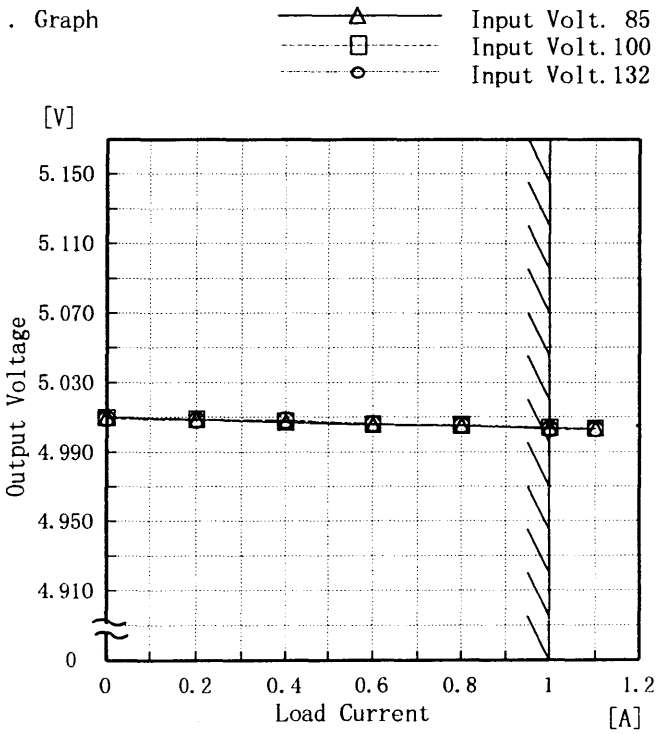
Model	YS505A
Item	Load Regulation 静的負荷変動
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
0.0	5.010	5.010	5.010
0.2	5.009	5.009	5.008
0.4	5.007	5.008	5.008
0.6	5.006	5.006	5.006
0.8	5.005	5.006	5.005
1.0	5.004	5.004	5.003
1.1	5.003	5.003	5.003
—	—	—	—
—	—	—	—
—	—	—	—

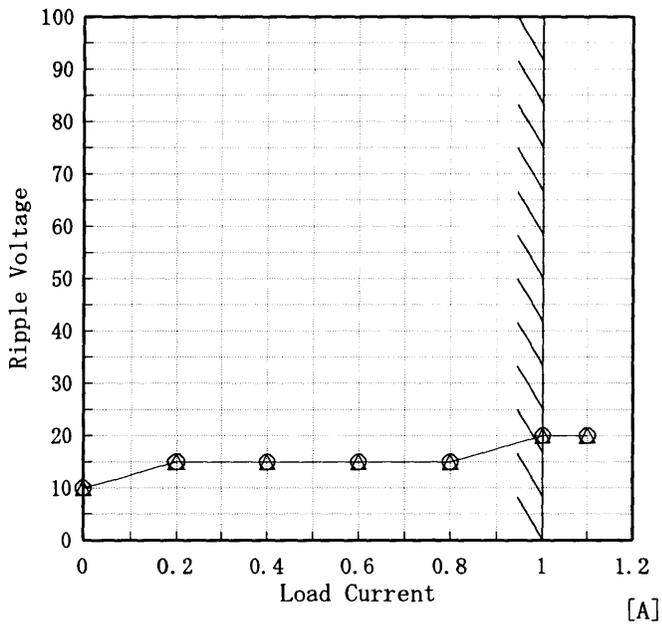
COSEL

Model	YS505A
Item	Ripple Voltage(by Load Current) リップル電圧(負荷特性)
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph

[mV]



Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

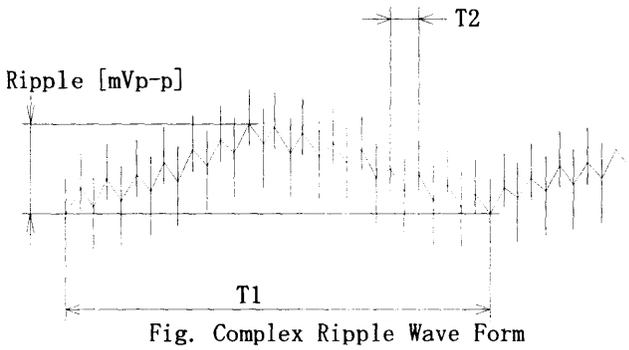


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

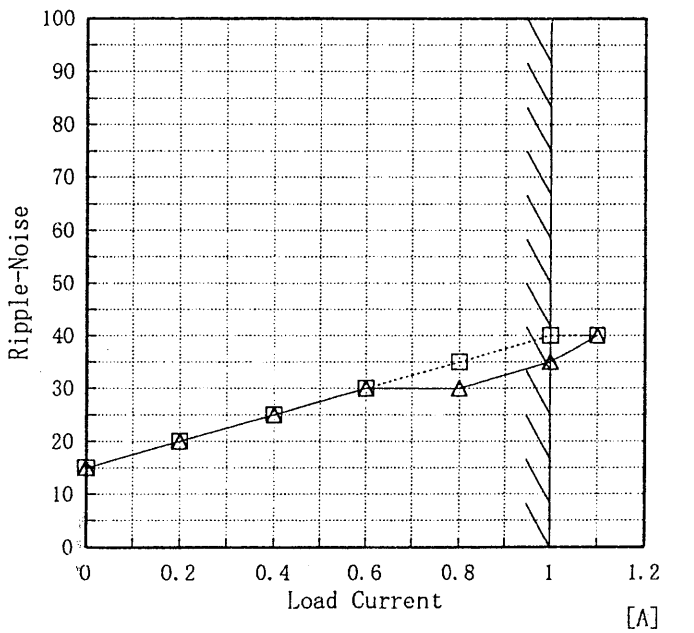
Load Current [A]	Ripple Output Voltage [mV]	
	Input Volt. 85 [V]	Input Volt. 132 [V]
0.0	10	10
0.2	15	15
0.4	15	15
0.6	15	15
0.8	15	15
1.0	20	20
1.1	20	20
—	—	—
—	—	—
—	—	—
—	—	—

COSEL

Model	YS505A
Item	Ripple-Noise リップルノイズ
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Ripple-Noise is shown as p-p in the figure below.
Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図ｐ－ｐ値で示される。
(注)斜線は定格負荷電流範囲を示す。

T1: Due to AC Input Line
入力商用周期
T2: Due to Switching
ｽｲｯﾁﾝｸﾞ周期

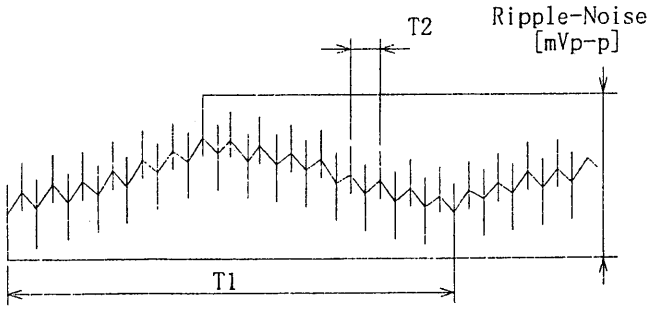


Fig. Complex Ripple Wave Form
図 リップル波形詳細図

2. Values

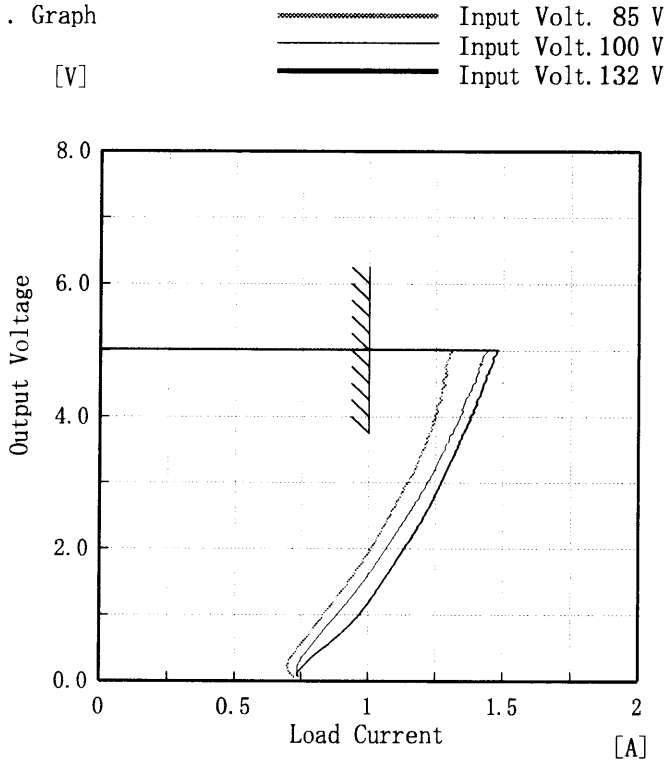
Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 85 [V]	Input Volt. 132 [V]
0.0	15	15
0.2	20	20
0.4	25	25
0.6	30	30
0.8	35	30
1.0	40	35
1.1	40	40
—	—	—
—	—	—
—	—	—
—	—	—

COSEL

Model	YS505A
Item	Overcurrent Protection 過電流保護
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
5.00	1.31	1.44	1.47
4.75	1.28	1.40	1.45
4.50	1.28	1.39	1.42
4.00	1.24	1.34	1.38
3.50	1.20	1.29	1.33
3.00	1.15	1.22	1.27
2.50	1.09	1.15	1.21
2.00	1.01	1.07	1.14
1.50	0.93	0.99	1.06
1.00	0.84	0.89	0.97
0.50	0.75	0.78	0.84
0.00	0.73	0.74	0.74

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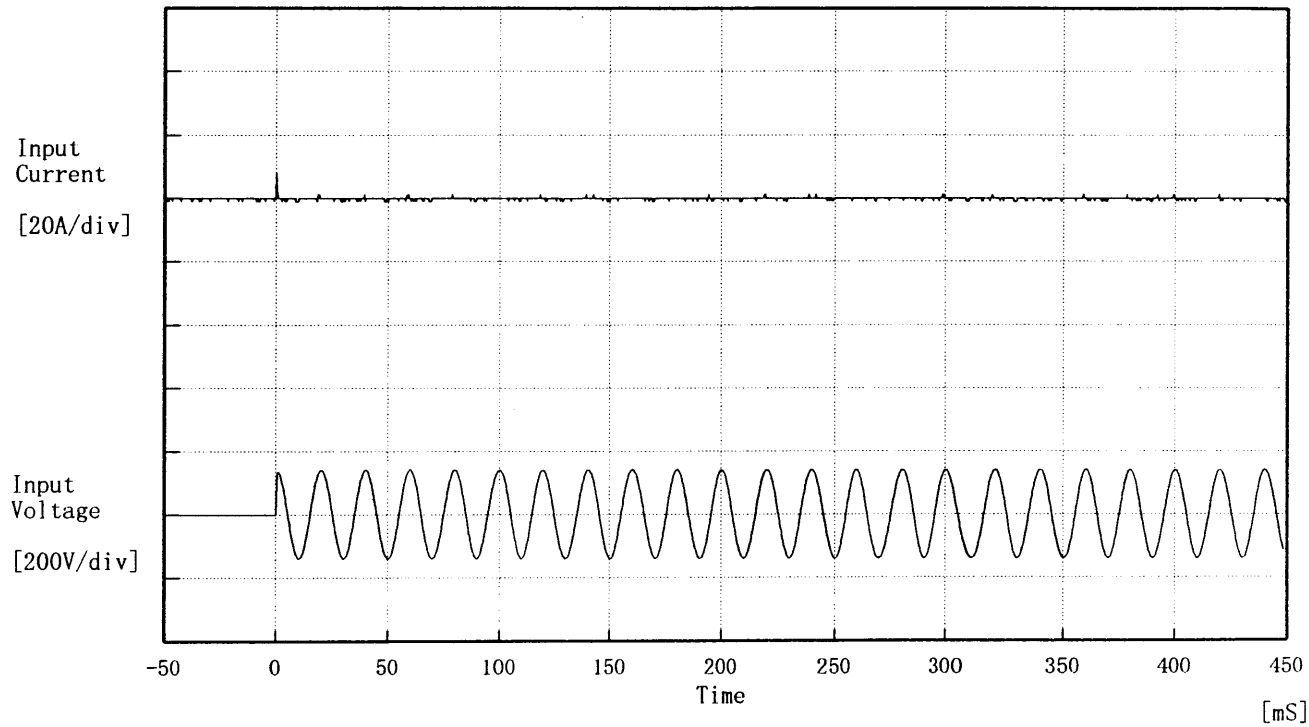
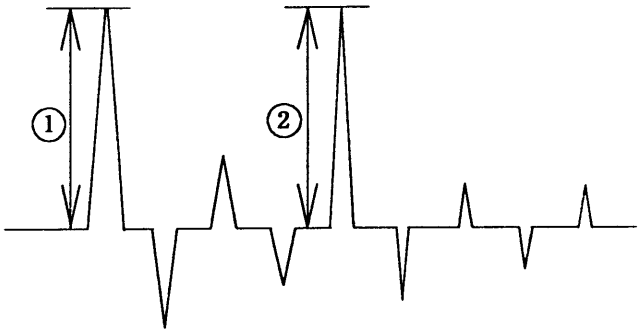
Model	YS505A	Temperature	25℃
Item	Inrush Current 突入電流	Testing Circuitry	Figure A
Object	――――――		



Input Voltage 100 V
Frequency 50 Hz
Load 100 %
Inrush Current
① 7.85 [A]
② 1.12 [A]



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Model	YS505A		
Item	Dynamic Load Responce 動的負荷変動	Temperature Testing Circuitry	25℃ Figure A
Object	+5.0V1A		

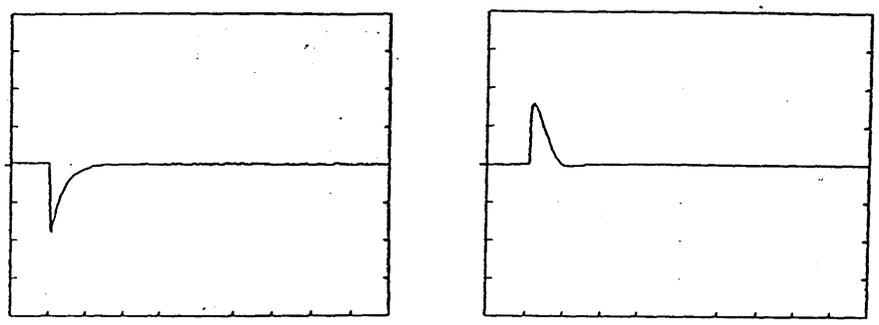
Input Volt. 100 V

Cycle 1000 mS

Load Current

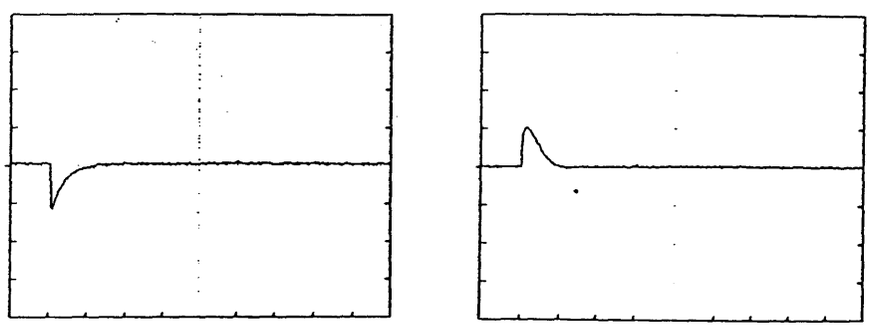
Load 0% ←→

Load 100 %



Load 0% ←→

Load 50 %



200 mV/div

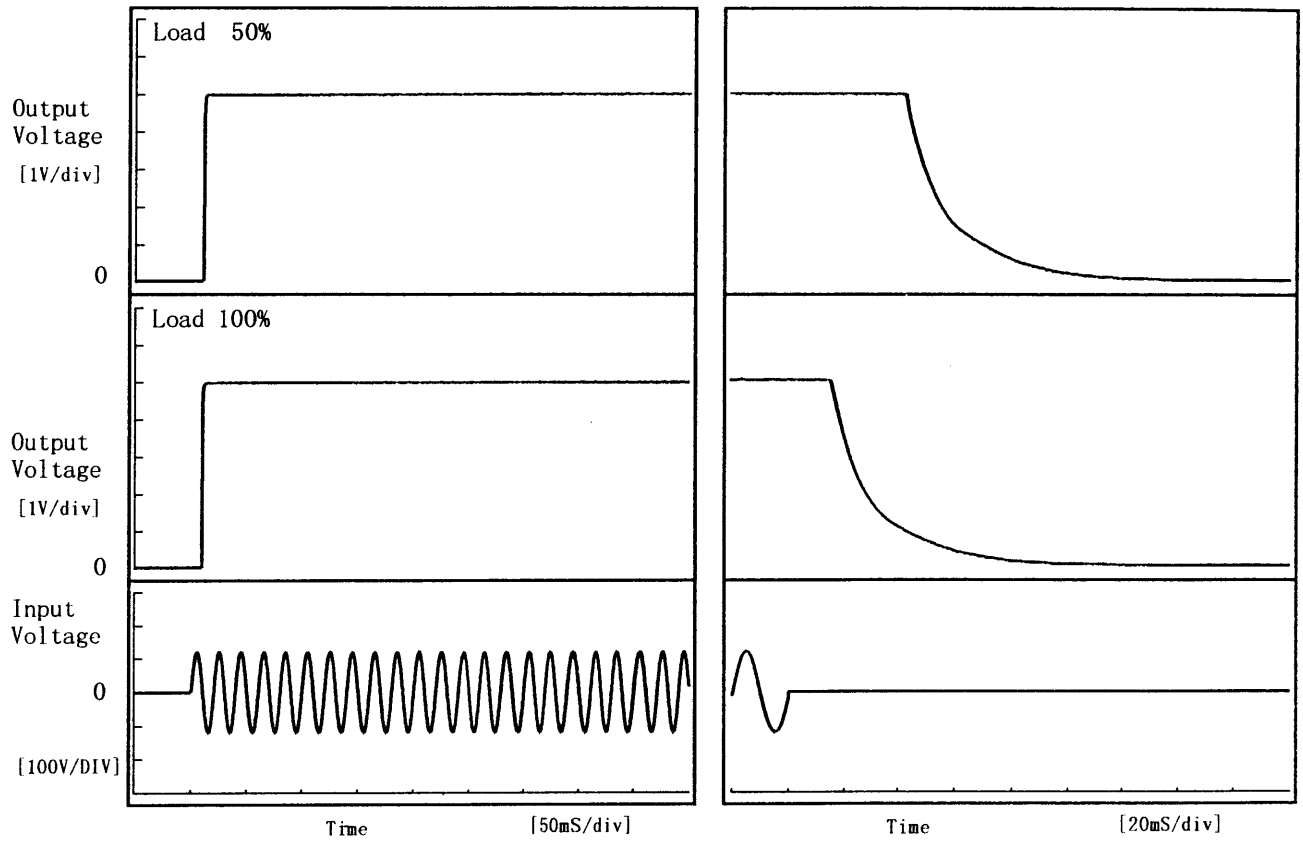
1 mS/div

COSEL

Model	YS505A		
Item	Rise and Fall Time 立上り、立下り時間	Temperature Testing Circuitry	25℃ Figure A
Object	+5.0V1A		

1. Graph

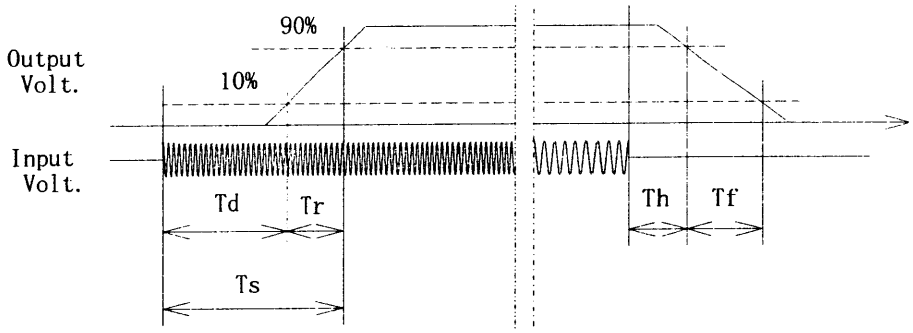
Input Volt. 85 V



2. Values

[mS]

Load \ Time	Td	Tr	Ts	Th	Tf
50 %	10.0	0.8	10.8	44.1	39.8
100 %	9.0	1.3	10.3	17.2	40.1

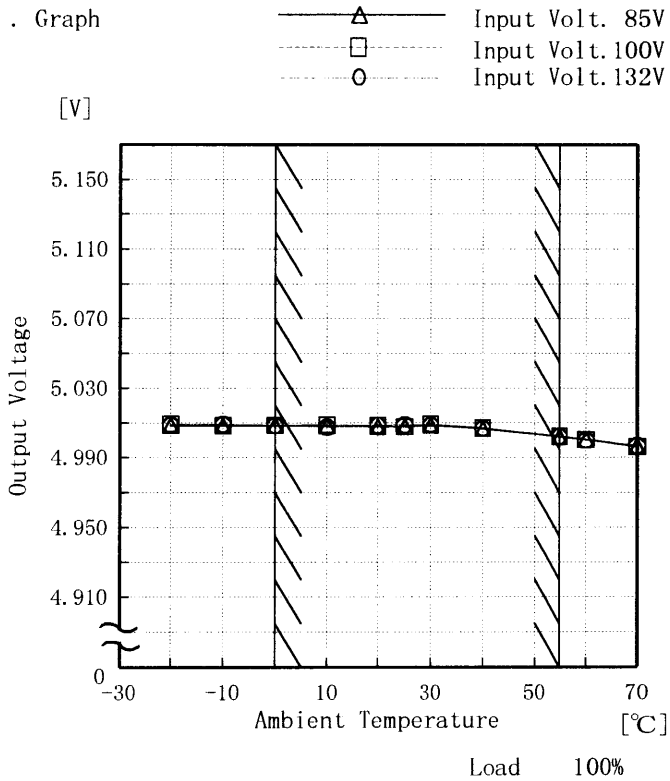


COSEL

Model	YS505A
Item	Ambient Temperature Drift 周囲温度変動
Object	+5.0V1A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

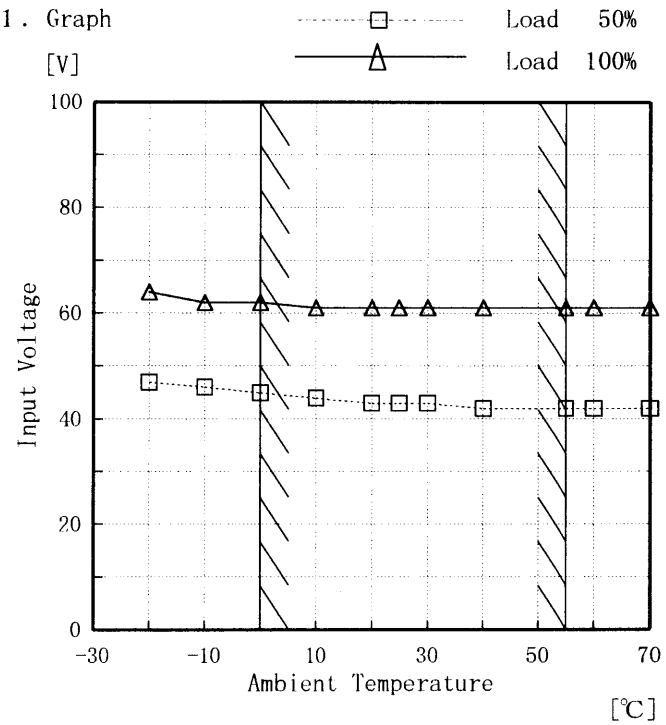
Ambient Temperature [℃]	Output Voltage [V]		
	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
-20	5.009	5.009	5.009
-10	5.008	5.009	5.009
0	5.009	5.008	5.008
10	5.008	5.009	5.008
20	5.008	5.009	5.009
25	5.008	5.008	5.009
30	5.009	5.009	5.009
40	5.006	5.007	5.007
55	5.002	5.002	5.002
60	5.000	5.000	5.000
70	4.996	4.996	4.997

COSEL

Model	YS505A
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+5.0V1A

Testing Circuitry Figure A

1. Graph



Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

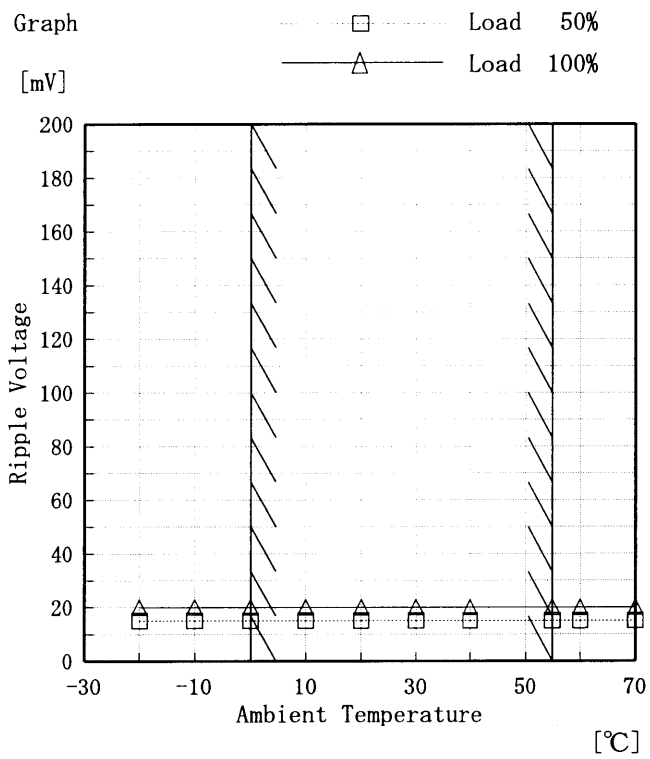
Ambient Temperature [℃]	Input Voltage [V]	
	Load 50%	Load 100%
-20	47	64
-10	46	62
0	45	62
10	44	61
20	43	61
25	43	61
30	43	61
40	42	61
55	42	61
60	42	61
70	42	61

COSEL

Model	YS505A
Item	Ripple Voltage (by Ambient Temp.) リップル電圧（周囲温度特性）
Object	+5.0V1A

Testing Circuitry Figure A

1. Graph



Input Volt. 85 V

Note: Slanted line shows the range of the rated ambient temperature.

(注)斜線は定格周囲温度範囲を示す。

2. Values

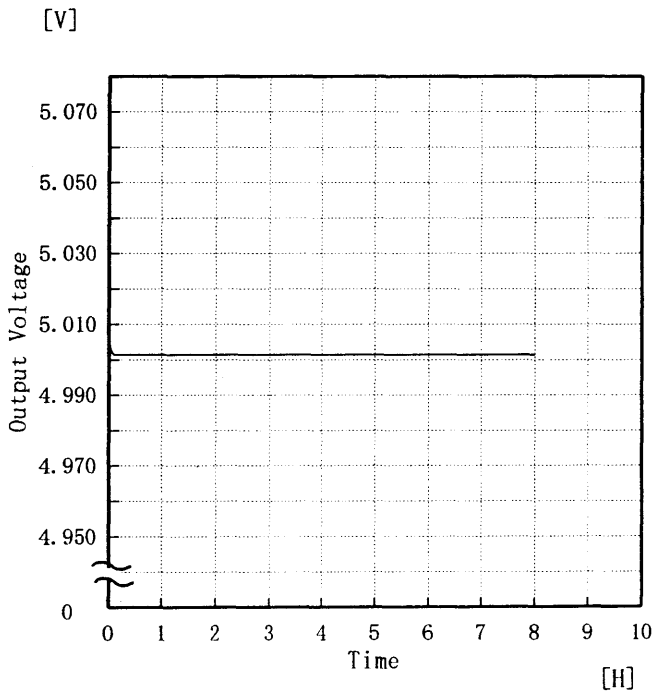
Ambient Temperature [℃]	Ripple Output Voltage [mV]	
	Load 50%	Load 100%
-20	15	20
-10	15	20
0	15	20
10	15	20
20	15	20
30	15	20
40	15	20
55	15	20
60	15	20
70	15	20
—	—	—

COSEL

Model	YS505A
Item	Time Lapse Drift 経時ドリフト
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Input Volt. 100V
Load 100%

2. Values

Time since start [H]	Output Voltage [V]
0.0	5.004
0.5	5.002
1.0	5.002
2.0	5.002
3.0	5.002
4.0	5.002
5.0	5.002
6.0	5.002
7.0	5.002
8.0	5.002

COSEL

Model	YS505A	
Item	Output Voltage Accuracy 定電圧精度	Testing Circuitry Figure A
Object	+5.0V1A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature 0～55 ℃
Input Voltage : 85～132 V
Load Current : 0～1 A

* Output Voltage Accuracy = ±(Maximum of Output Voltage − Minimum of Output Voltage)／2

$$* \text{ Output Voltage Accuracy (Ration)} = \frac{\text{Output Voltage Accuracy}}{\text{Rated Output Voltage}} \times 100$$

1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 0～55 ℃
入力電圧 85～132 V
負荷電流 0～1 A

* 定電圧精度(変動値) ＝ ±(出力電圧の最高値－出力電圧の最低値)／2

$$* \text{ 定電圧精度(変動率)} = \frac{\text{変動値}}{\text{定格出力電圧}} \times 100$$

2. Values

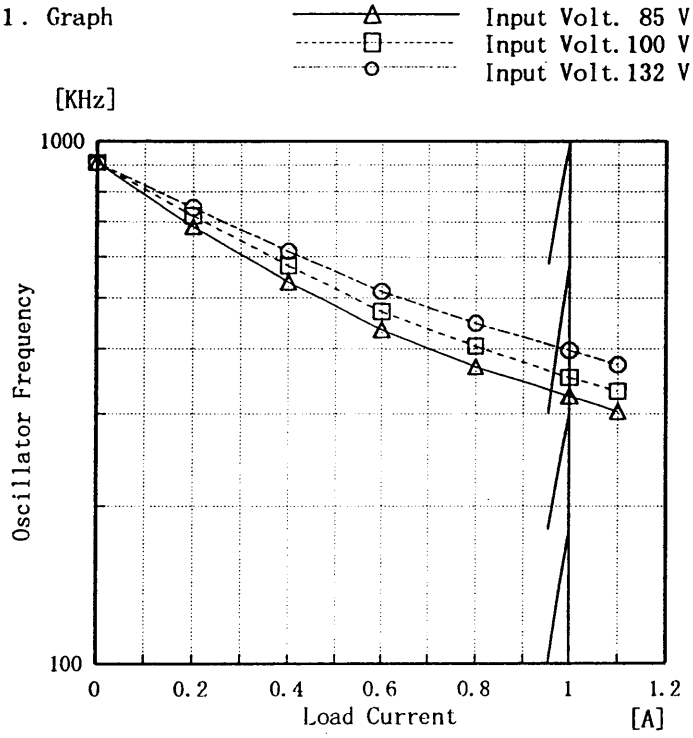
Item	Temperature [℃]	Input Voltage [V]	Output Current [A]	Output Voltage [V]	Output Voltage Accuracy [mV]	Output Voltage Accuracy (Ration) [%]
Maximum Voltage	25	85	0	5.010	±4	±0.1
Minimum Voltage	55	85	1	5.002		

COSEL

Model	YS505A
Item	Oscillator Frequency 発振周波数
Object	+5.0V1A

Temperature 25℃
Testing Circuitry Figure A

1. Graph



Note:Slanted line shows the range of the rated load current.

(注)斜線は定格負荷電流範囲を示す。

2. Values

Load Current [A]	Input Volt. 85[V]	Input Volt. 100[V]	Input Volt. 132[V]
	Oscillator Frequency [KHz]		
0.0	909	909	909
0.2	685	719	746
0.4	536	576	614
0.6	435	471	515
0.8	370	405	448
1.0	324	352	397
1.1	303	331	373
—	—	—	—
—	—	—	—
—	—	—	—
—	—	—	—

COSEL

Model	YS505A
Item	Condensation 結露特性
Object	+5.0V1A

Testing Circuitry Figure A

1. Condensation test

Testing procedure is as follows.

① Keeping and cooling the unit in a tank at -10℃ for an hour with the input off.

② Taking it out of the tank and dewing itself in a room where the temperature is 25℃ and the humidity is 40%RH.

③ Testing electrical characteristics of the unit to confirm there be no fault.

1．結露特性試験

入力を切った状態で、恒温槽で－１０℃に冷却しておき、約１時間後に恒温槽から取り出し、室温２５℃、湿度４０%RHの状態におき結露させ、その電気的特性の測定を行い、異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	5.003	Input Volt.: 100V, Load Current:1A
Line Regulation [mV]	2	Input Volt.: 85～132V, Load Current:1A
Load Regulation [mV]	7	Input Volt.: 100V, Load Current:0～1A

COSEL

Model	YS505A		
Item	Leakage Current 漏洩電流	Temperature Testing Circuitry	25℃ Figure B
Object	――――――		

1. Results

Standards	Leakage Current [mA]		
	Input Volt. 85 [V]	Input Volt. 100 [V]	Input Volt. 132 [V]
(A) DENTORI	0.16	0.18	0.25
(B) IEC60950	0.15	0.18	0.24

Standards	Leakage Current [mA]		
	Input Volt. 170 [V]	Input Volt. 230 [V]	Input Volt. 264 [V]
(B) IEC60950	—	—	—

2. Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

交流入力の両相について測定し、その大きい方を漏洩電流測定値とする。

COSEL

Model	YS505A		
Item	Line Noise Tolerance 入力雑音耐量	Temperature Testing Circuitry	25℃ Figure C
Object	+5.0V1A		

1. Results

Pulse Width [nS]	MODE	No protection failure should occur 保護回路の誤動作がない	DC-like Regulation of Output Voltage 出力電圧の直流的変動
50	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation
1000	COMMON	OK	no fluctuation
	NORMAL	OK	no fluctuation

2. Conditions

Input Voltage : 100 V
Pulse Voltage : 2000 V
Pulse Cycle : 10 mS
Pulse Input Duration : 1 min. or more
Load : 100 %

COSEL

Model	YS505A
Item	Conducted Emission 雑音端子電圧
Object	――――――

Testing Circuitry　　Figure D

1. Graph

Remarks

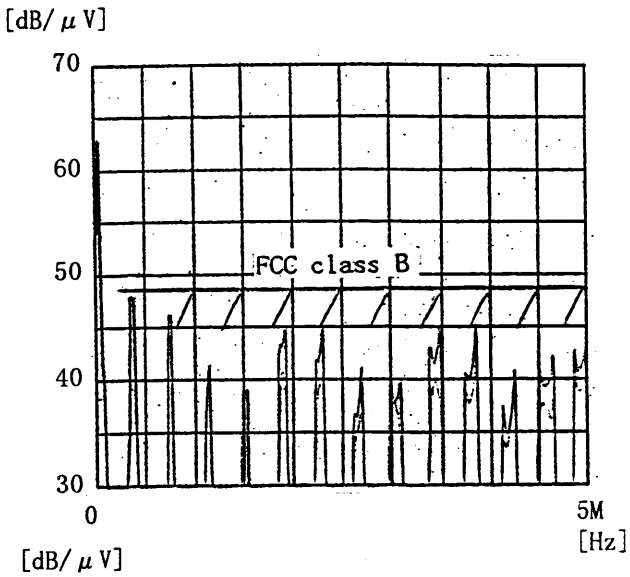
Input Volt.　120 V

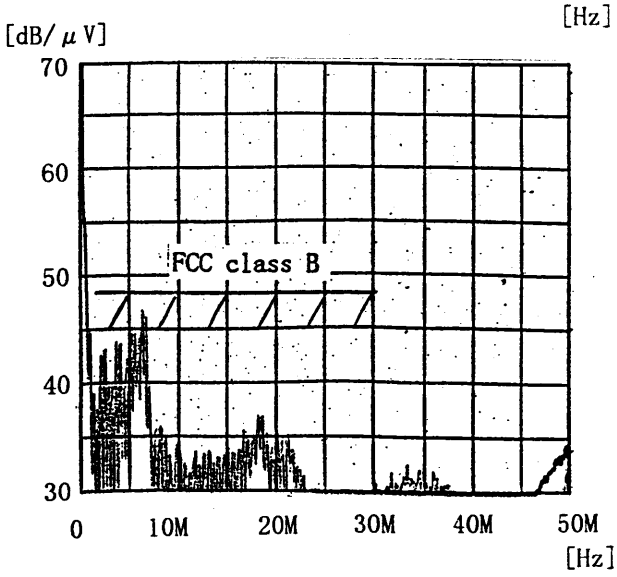
Load　100 %

Note: Slanted line shows the range of Tolerance.

(注)斜線は許容値を示す。

NO	Standards	Standards Complied	Frequency [MHz]	Tolerance [dB/μV]
1	FCC class A		0.45～1.6	60
			1.6～30	69.5
2	FCC class B	○	0.45～30	48
3	VCCI class A		0.15～0.5	79
			0.5～30	73
4	VCCI class B		0.15～0.5	66-56
			0.5～5	56
			5～30	60
5	CISPR Pub. 22 class A (EN55022)		0.15～0.5	79
			0.5～30	73
6	CISPR Pub. 22 class B (EN55022)		0.15～0.5	66-56
			0.5～5	56
			5～30	60





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COSEL

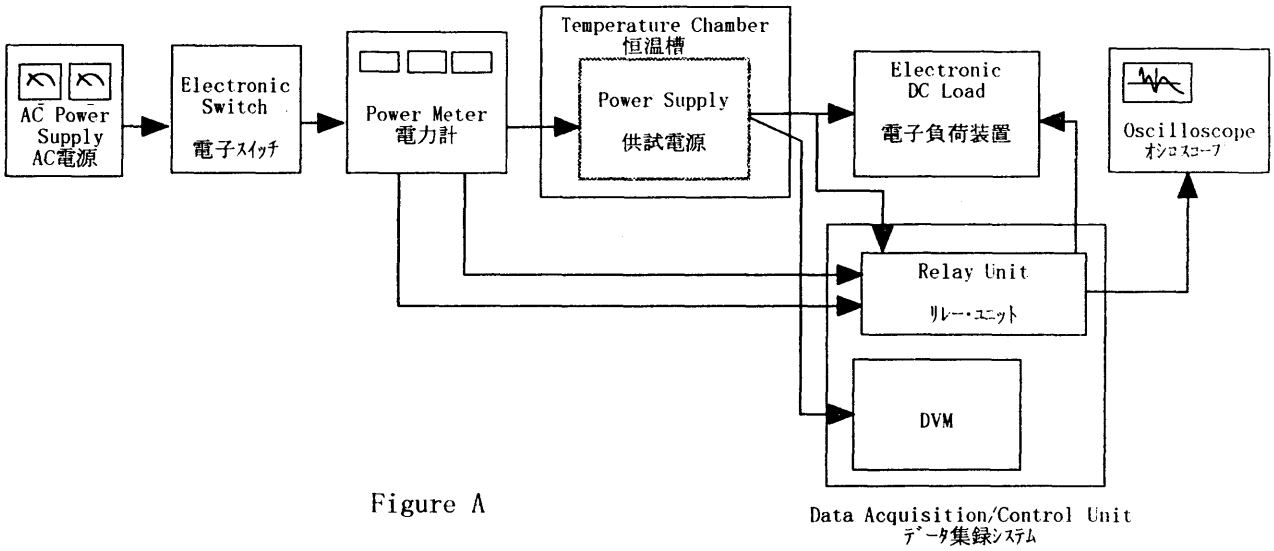

Figure A

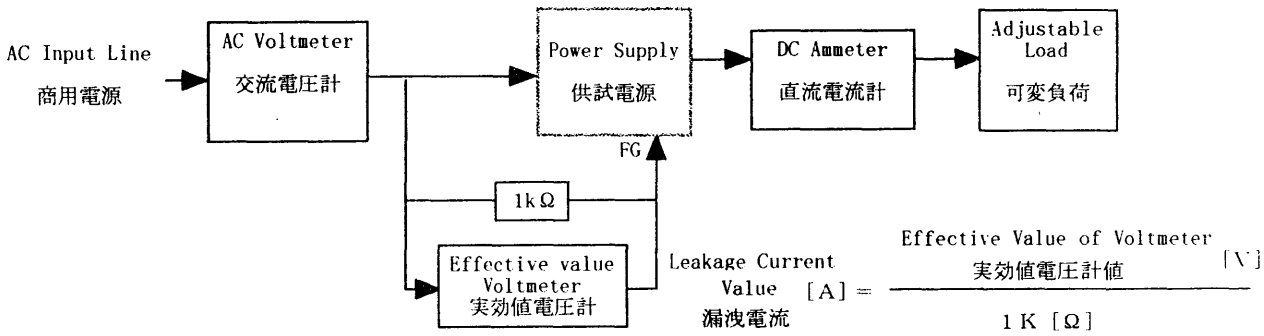

Figure B(DENTORI)

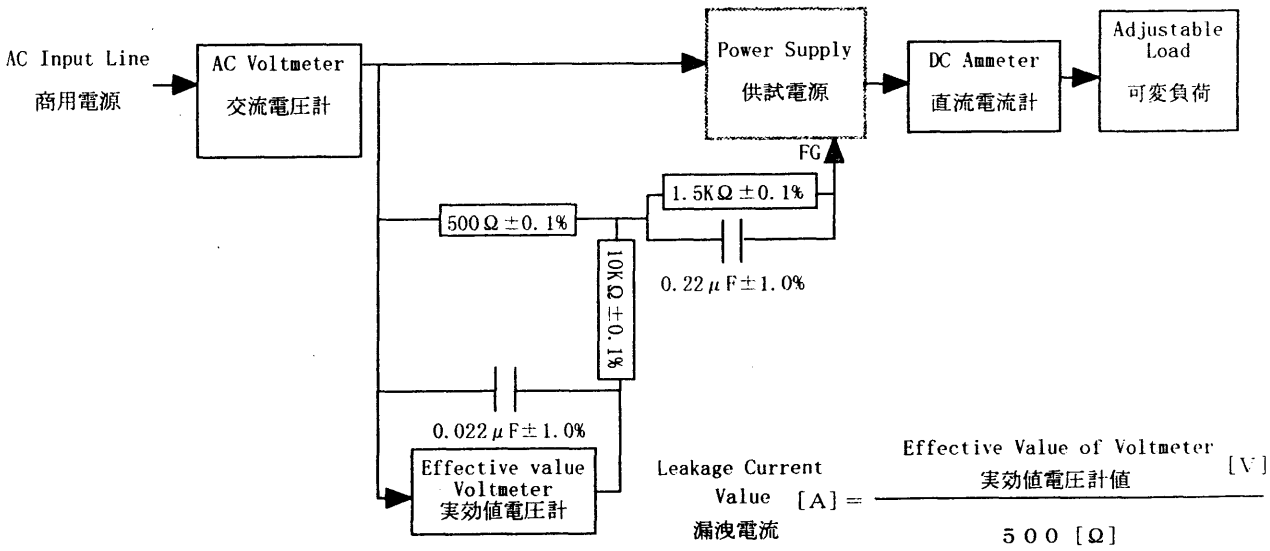

Figure B (IEC60950)

COSEL

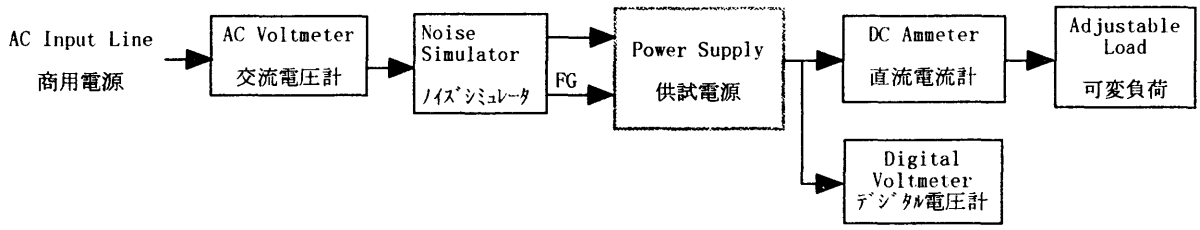

Figure C

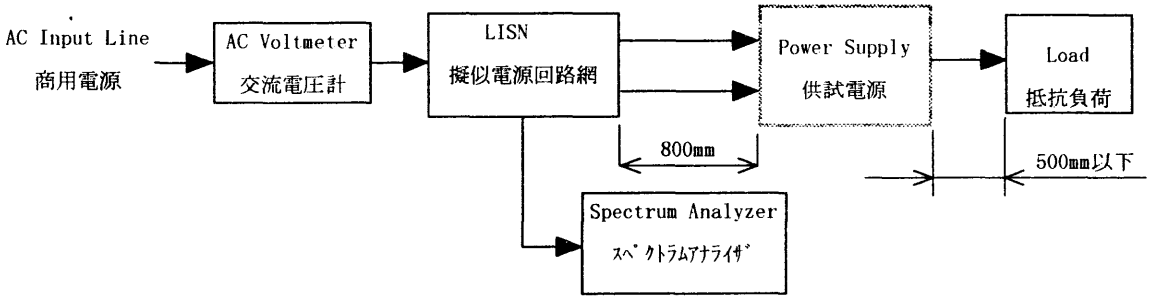

Figure D

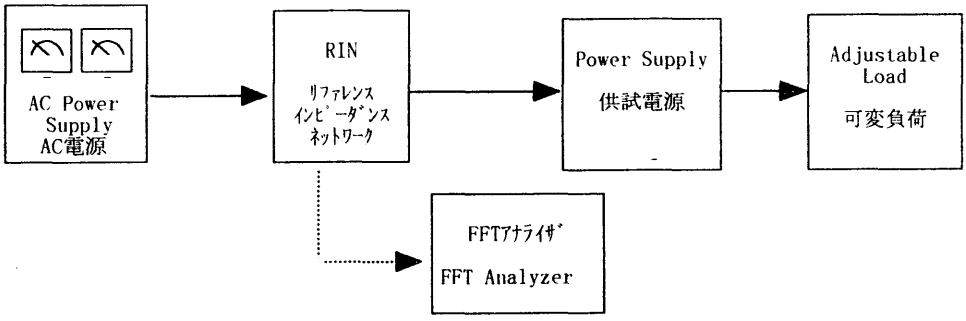

Figure E